### **ORIGINAL RESEARCH**

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# Active power based distance protection scheme in the presence of series compensators

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#### **Abstract**

Flexible ac transmission system (FACTS) controllers, especially the series-FACTS controllers, affect the operation of distance relays and can lead to the relays under/over-reaching. This paper aims to demonstrate the effects of static synchronous series compensator (SSSC) and series capacitive compensation (SCC), as two important series compensators, on the distance protection using theoretical and computational methods. The results of the investigation are used to develop a feasible and adequate method for eliminating the negative effects of these devices on the distance relays. The developed method measures the voltages at terminals of the SSSC and SCC by phasor measurement units (PMUs) which are then transmitted to the relay location by communication channels. The transmitted signals are used to modify the voltage measured by the relay. Different operation types and conditions of SSSC and SCC, and different faults such as phase-to-phase and phase-to-ground faults are investigated in simulations. Since the modeled distance relay can measure the fault resistance, trip boundaries are used to show the performance of the presented method. Results show that the presented method properly eliminates the negative effects on the distance relays and prevents them from mal-operation under all fault resistance conditions.

#### Introduction

The operation of FACTS controllers and their response to the variations in the power system, either made by operators or faults, is sufficiently fast to affect the voltage and current signals measured by the protection relays. These variations in the signals produce a substantial delay in the relay's operation and usually lead to their under-reaching. There have been many efforts dedicated to investigating these effects in different power protection systems. Majority of the studies have tackled these effects on impedance relays like the distance relays of transmission lines and loss-of-excitation (LOE) relays of synchronous generators, both of which are based on impedance measurements. Investigations in [1–11] analyze the distance relay performance in the presence of FACTS controllers. These studies can be categorized into three major parts based on different compensation devices: a) shunt-FACTS like static Var compensator

In the aforementioned investigations, the author has justified the existing FACTS controllers effect on the *mho* impedance relays operation and therefore, it is

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<sup>(</sup>SVC) and static synchronous compensator (STATCOM) [1–4], b) series-FACTS like SSSC and thyristor controlled series capacitor (TCSC) [5-7], and c) shuntseries-FACTS like generalized interline power flow controller (GIPFC) and unified power flow controller (UPFC) [8-11]. In [7], the impact of SSSC on distance relay performance was studied. In this paper, conventional distance relay was used with no capability of  $R_f$ calculation. The paper did not present any algorithm to remove the impact of SSSC on the relay performance. The results in these papers show that the series and shunt-series FACTS controllers have severer negative effects on the operation of distance relays than other devices due to the increased zero component of the injected voltage along the fault. Also, it is shown that the most severe effect occurs under phase-to-ground fault condition which usually results in the relays under-

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necessary to revise the protection algorithm of these relays to eliminate or at least decrease the negative effects on them. Majority of recent studies use the PMUs to measure the voltage and current which are injected by the FACTS because these controllers are usually located in the middle of the transmission line and it is necessary to transmit the measured data to the relays location. This method is used in [11] to modify the distance relay operation in the presence of UPFC using a generalized regression neural network algorithm. In [12], the signals of the PMUs, which are located at both ends of the transmission line, are transmitted to relay location to eliminate the negative effects of GIPFC on the impedance based loss-of-excitation relay. Also, investigations in [13-15] utilize PMUs to eliminate the effects of the series capacitive compensators on the distance relays. In [16], the synchrophasors are used to mitigate the effects of phase-shifting transformer on the distance relay.

This paper shows that the commonly used series compensators, e.g. SSSC and SCC, can cause the distance relays mal-operation and distort the backup protection. A simple and feasible method which uses the equivalent circuit of the SSSC and SCC to modify the distance relays is then presented to eliminate the detrimental effects of the SSSC and SCC on the measured impedance of the distance relays. Synchrophasors are used to calculate the voltage and current signals in the buses and communication channels are used to transfer them to the system protection center (SPC). Consequently, according to the developed new algorithm, fault resistance and compensator effects are removed and fault location can be precisely detected. In [11-16], the presented methods were used for faults with small fault resistance  $(R_f)$  or only improved the performance of Zone-3. In addition, the studied algorithms were complicated and the SSSC was not always considered. In contrast, this study considers all of the mentioned issues and the results show that the presented method eliminates the negative effects of the SSSC and SCC under all of the different operation conditions. Signal transmission delay is also counted in the simulations and it shows that the presented method does not slow down the response of the relay.

#### Modeling of system with SSSC and distance relays

The power system under study as shown in Fig. 1a comprises three transmission lines each with 200 km lengths. The series compensator is located at the middle of line-2 and the distance relays, each with three protection Zones, are located at the beginning of the lines. For example, Zone-1 of the relay  $R_A$ , comprises 80% of the transmission line-1; Zone-2 comprises the whole of line-1 and 50% of line-2; and Zone-3 comprises the whole of lines-1 and 2 and also 20% of line-3. The other relays have been set similarly to  $R_A$ . Also, applying delays to

the operation of Zones-2 and 3 of the distance relays creates the backup protection. The modelled distance relays can measure the fault resistance. Since high-resistance faults cause the relays under-reaching, different methods have been presented to eliminate the under-reaching [16–21]. The feasible and simple method of [16] and [21] is used in this study in which the currents and active powers are measured at the buses of the transmission line with PMUs. These signals are then transmitted to the relay location to calculate  $I_f = I_C + I_B$  and  $R_f$  is calculated as:

$$R_{f} = \frac{P_{B} + P_{C} - xR_{L2}(I_{B})^{2} - (1 - x)R_{L2}(I_{C})^{2}}{(I_{B} + I_{C})^{2}}$$
(1)

Since the line resistance is negligible in comparison with the fault resistance, it is omitted from (1) and thus it can be simplified as:

$$R_f \cong \frac{P_B + P_C}{\left(I_f\right)^2} \tag{2}$$

Once  $R_f$  is known, its effects can be easily eliminated on the A–G element. The detailed model of the SSSC which comprises a 48-pulse voltage source convertor is present in MATLAB [22] and is used in the modeling here. The SCC model also has a fixed capacitor and a parallel controlled switch in each of the phases. The characteristics of the power system are presented in [23]. Power system parameters have been provided in appendix.

#### Impact of SSSC and SCC on distance protection

The positive, zero and negative sequence networks of the power system from the viewpoint of the  $R_B$  relay are shown in Fig. 1b. In this figure, the series compensators are considered as a variable voltage source because the SSSC acts like a controllable series voltage source. Also, SCC has a series impedance making the SCC like a variable or controllable voltage source while the current following through this impedance. The positive sequence voltage at the  $R_B$  relay location ( $V_{IB}$ ) can be expressed as:

$$V_{1B} = xZ_{1L}I_{1B} + R_fI_{1f} + \Delta V_1 + V_{1f}$$
 (3)

The negative  $(V_{2B})$  and zero  $(V_{0B})$  sequence voltages are obtained from Fig. 1 in the same way as:

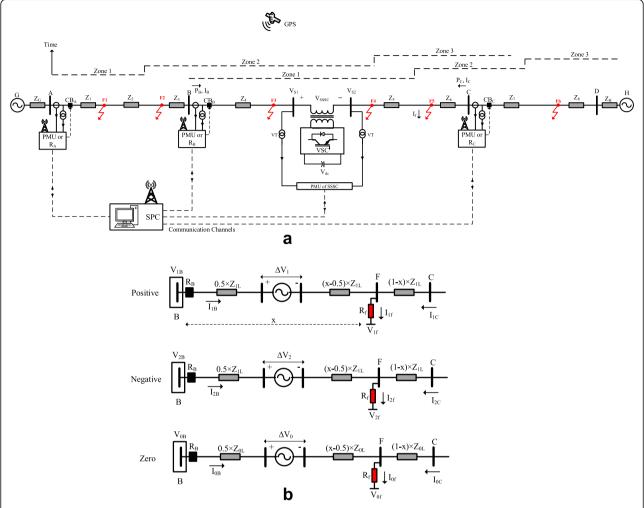
$$V_{2B} = xZ_{1L}I_{2B} + R_fI_{2f} + \Delta V_2 + V_{2f}$$
(4)

and

$$V_{0B} = xZ_{0L}I_{0B} + R_fI_{0f} + \Delta V_0 + V_{0f}$$
 (5)

For a single phase-to-ground fault, the following equations can be obtained:

$$I_B = I_{1B} + I_{2B} + I_{0B} \tag{6}$$



**Fig. 1** Single-line diagram multi-line system. **a** with SSSC. **b** positive, negative and zero sequences networks of the power system from the viewpoint of the  $R_B$  for an A–G fault on line–2

$$I_f = I_{1f} + I_{2f} + I_{0f} (7)$$

$$\Delta V = \Delta V_1 + \Delta V_2 + \Delta V_0 \tag{8}$$

and

$$V_B = V_{1B} + V_{2B} + V_{0B} (9)$$

Substituting the calculated  $V_{1B}$ ,  $V_{2B}$  and  $V_{0B}$  into (9) and considering the fact that  $V_{1f} + V_{2f} + V_{0f} = 0$  is valid for single phase-to-ground fault, following equation can be derived:

$$V_{B} = V_{1B} + V_{2B} + V_{0B}$$

$$= xZ_{1L} \underbrace{\left(I_{1B} + I_{2B} + I_{0B}\right)}_{I_{B}} - xZ_{1L}I_{0B}$$

$$+ R_{f} \underbrace{\left(I_{1f} + I_{2f} + I_{0f}\right)}_{I_{f}} + xZ_{0L}I_{0B}$$

$$+ \underbrace{\left(\Delta V_{1} + \Delta V_{2} + \Delta V_{0}\right)}_{\Delta V}$$

$$= xZ_{1L}I_{B} + xI_{0B}(Z_{0L} - Z_{1L}) + R_{f}I_{f} + \Delta V$$
(10)

For a single phase-to-ground fault (A-G) on line-2, the apparent impedance seen by  $R_B$  relay is as follows [24].

$$Z_{A-G} = \frac{V_B}{I_B + I_{0B}((Z_{0L} - Z_{1L})/Z_{1L})} = \frac{V_B}{I_{A-G}}$$

$$= xZ_{1L} + \underbrace{R_f \frac{I_f}{I_{A-G}}}_{\Delta Z_{Rf}} + \underbrace{\frac{\Delta V}{I_{A-G}}}_{\Delta Z_{A-G}}$$
(11)

For a single phase-to-ground fault, the impact of series compensator on the apparent impedance is expressed by  $\Delta Z_{A-G}$  and the series voltage of the compensator has a direct impact on the apparent impedance. For a phase-to-phase fault, one can write:

$$Z_{A-B} = \frac{V_{1B} - a \cdot V_{2B}}{I_{1B} - a \cdot I_{2B}} \tag{12}$$

where a = -0.5 + j0.866. Substituting  $V_{1B}$  and  $V_{2B}$  from (3) and (4) into (12) yields:

$$Z_{A-B} = \frac{V_{1B} - aV_{2B}}{I_{1B} - aI_{2B}}$$

$$= \frac{(xZ_{1L}I_{1B} + R_{f}I_{1f} + \Delta V_{1} + V_{1f}) - a(xZ_{1L}I_{2B} + R_{f}I_{2f} + \Delta V_{2} + V_{2f})}{I_{1B} - aI_{2B}}$$
(13)

and

$$Z_{A-B} = \frac{xZ_{1L}(I_{1B} - aI_{2B}) + R_f(I_{1f} - aI_{2f}) + (\Delta V_1 - a\Delta V_2) + (V_{1f} - aV_{2f})}{I_{1B} - aI_{2B}}$$

Simplifying (14) and taking into account of  $V_{If} + aV_{2f} = 0$  in case of A–B fault yield:

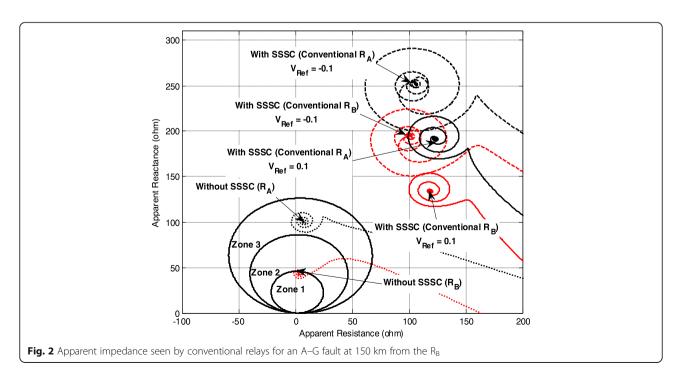
$$Z_{A-B} = xZ_{1L} + \underbrace{R_f \frac{(I_{1f} - aI_{2f})}{I_{1B} - aI_{2B}}}_{\Delta Z_{Rf}} + \underbrace{\frac{(\Delta V_1 - a\Delta V_2)}{I_{1B} - aI_{2B}}}_{\Delta Z_{A-B}}$$
(15)

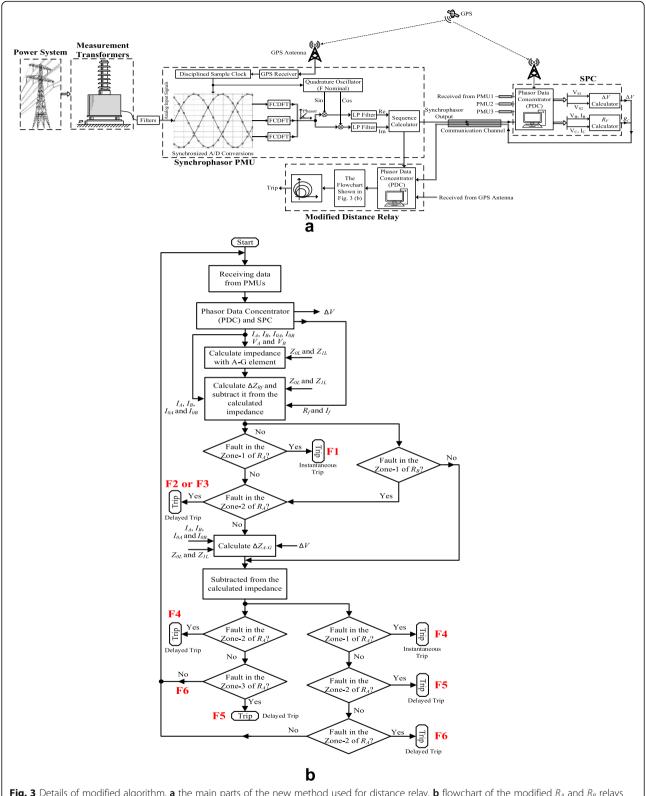
 $R_f$  is the fault resistance between the two phases in (15). For a phase-to-phase fault, the impact of compensator on the apparent impedance is expressed by  $\Delta Z_{A-B}$ . Since the series compensator affects the calculated impedance via the voltage across the terminals ( $\Delta V$ ), if this voltage is known and is transmitted to the relay location, the problems emanating from these voltage changes can be solved. In section IV, this method is investigated by simulations. Simulation results for single phase-to-ground fault, A–G, 150 km away from  $R_B$  relay (F1 fault)

are shown in Fig. 2. This fault occurs at Zone-2 of  $R_A$ relay and Zone-1 of  $R_B$  relay. Presented results in Fig. 2 show that the relays detect the fault in their correct zones when the SSSC is not connected to line-2. The presence of SSSC increases the impedance calculated by the relays and makes both relays to under-reach. These under-reaching are very severe, and consequently, the fault falls into none of the protection zones of the relays. The results presented in Fig. 2 include both capacitive  $(V_{Ref} = 0.1)$  and inductive  $(V_{Ref} = -0.1)$  operation modes of the SSSC. As it can be seen in this figure, the SSSC causes relays under-reaching during both capacitive and inductive operation modes, though it is more severe in inductive mode than in capacitive mode. The cause for under-reaching is due to the zero component of injected voltage as has been precisely explained in [7]. Exclusive discussions about the SSSC effects on distance relays operation are also presented in [7]. Herein, the main objective of the paper is to investigate the method for eliminating these negative effects.

#### **Modified distance protection**

Nowadays, synchrophasors are commonly used to improve the operation of power systems such as relays and stabilizers. This method uses PMUs to measure the required information at different locations of the power system and send them to the controlling center. These data can then be used to generate proper control signals to improve the operation of the system. Communication channels such as optical fibers are used in this method to transmit the data. Since the data are sent from





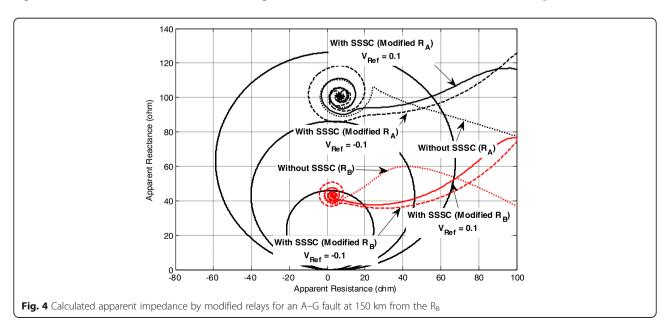
**Fig. 3** Details of modified algorithm. **a** the main parts of the new method used for distance relay. **b** flowchart of the modified  $R_A$  and  $R_B$  relays

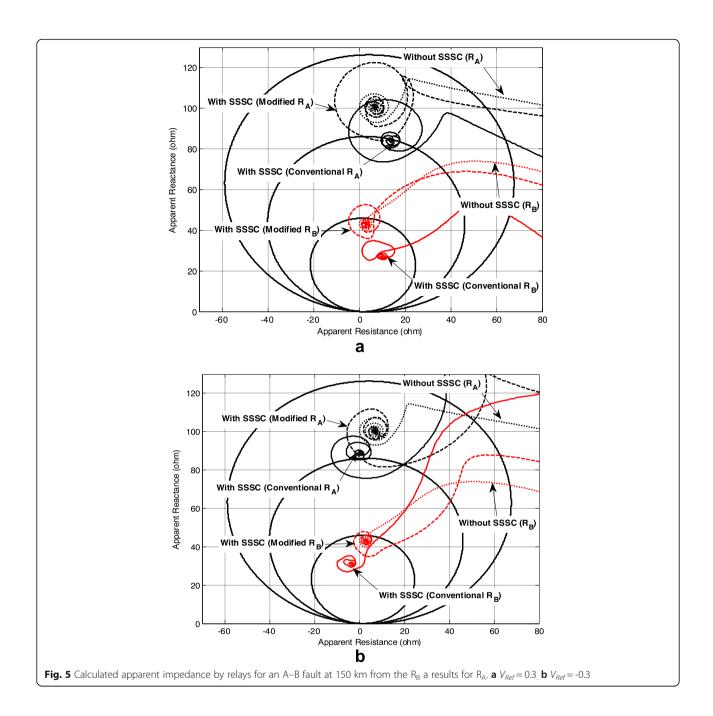
different locations with different delays, it is necessary to synchronize and make them time stamped using the global positioning system (GPS) [25, 26]. In the presented method, a relay or PMU is considered at the beginning of the transmission line. Existence PMU at the beginning of the transmission line-2 (bus B to C) plays the role of the PMU at the end of the transmission line-1 (bus A to B) [27]. Therefore, for the power system presented in Fig. 1a, the values of  $V_A$ ,  $V_B$ ,  $V_C$ ,  $I_A$ ,  $I_B$  and  $I_C$  signals are available in SPC. As seen in the theoretical analyses results, the presence of series compensators changes the impedance calculated by the relays caused by the equivalent voltage of the series compensators. To eliminate this issue, the voltages of both compensator terminals are measured as shown in Fig. 1a and are sent to SPC using communication channel. The method used in this paper is more precisely presented in Fig. 3a. It should also be mentioned that it is necessary to synchronize the relay signals with the signals sent by the PMUs from the compensator locations. The analog signals are filtered first and are then converted into digital signals by synchronized A/D conversions [28]. The phasors are calculated by the PMUs using the full cycle discrete fourier transform (FCDFT) method. The phasors are used to determine the sequences of the signals. Finally, voltage across the series compensator ( $\Delta V$ ) and the fault resistance ( $R_f$ ) are calculated and sent to the distance relays. The calculated signals are synchronized again using phasor data concentrator (PDC) and are used to determine the fault by the six elements of the relay. At the relay location, the  $\Delta V$  voltage is deducted from the relay measured voltage to eliminate the series compensator effect. Different locations are chosen for the faults as shown in Fig. 1a. The flowchart shown the modified algorithms of the  $R_A$  and  $R_B$  relays is given in Fig. 3b. Figure 1a and the flowchart show different scenarios about the fault (F1-F6) for different locations. The flowchart presents A-G element and will be the same for other faults, only the impedance calculation will change. Zone-2 of the  $R_A$ relay covers the middle of line-2 (bus B to C) but the SSSC is also located in the middle of line-2. Therefore, if the fault occurs in the right side of the SSSC in Fig. 1a, the SSSC will be located in the fault loop. However, if the fault occurs in the left side of the SSSC in Fig. 1a, the SSSC will not be located in the fault loop and the fault will be detected in the end of Zone-2 of  $R_A$  relay. These issues are presented in the flowchart with F3 and F4. Herein, the simulation results for different operation modes of the compensator and different types of faults are presented.

#### With SSSC

Simulation results for A–G fault in 150 km away from  $R_B$  relay are shown in Fig. 4. According to this figure, the modified method has eliminated the errors of the relays and made them capable of detecting the fault in the proper zone. The results for phase-to-phase fault (A–B) are presented in Fig. 5a and b for  $V_{Ref}$ = 0.3 and -0.3, respectively. Comparing these results with the phase-to-ground fault (Fig. 2) reveals that the phase-to-ground faults have severer effects on the operation of the relays. Furthermore, the phase-to-phase faults make the relay become over-reach. Similar to the phase-to-ground fault, the modified algorithm can eliminate the errors in the relay operation under phase-to-phase faults too.

The results for different fault types at different locations and under different SSSC operation modes are





presented in Table 1. The fault locations are intentionally chosen such that the SSSC falls into the desired fault loops. The distances in Table I show where the faults are located with respect to the  $R_A$  relay. The following equation is used to calculate the data presented in Table 1:

$$Error = |Z_{WO} - Z_W| \tag{16}$$

where  $Z_{WO}$  and  $Z_{W}$  are the relay measured impedance without and with the SSSC connected to the line

respectively. Regarding Table I for all the  $V_{Ref}$  values, the SSSC effect under phase-to-ground faults is more severe than phase-to-phase faults. For instance, the maximum impedance differences due to SSSC effect for A–G and A–B faults are 183 and 24.7  $\Omega$  respectively. For A–G faults, the SSSC effect decreases as  $V_{Ref}$  increases while for A–B faults the SSSC effect increases with increasing  $V_{Ref}$ . Also, the SSSC effect decreases as the distance between the A–G fault and the relay increases while its effect increases as the distance between the A–B fault and the relay increases. For

Table 1 Performance of conventional (CON) and modified (MOD) distance relay with the presence of SSSC and SCC

		Fault distance from $R_A$	310 km		350 km		400 km		450 km		
		Method	CON	MOD	CON	MOD	CON	MOD	CON	MOD	
With SSSC	A–G Fault	$V_{Ref} = 0.1 \text{ p.u}$	149 Ω	0.039 Ω	147 Ω	0.033 Ω	144 Ω	0.037 Ω	140 Ω	0.125 Ω	Calculated by (16)
		$V_{Ref} = 0.3 \text{ p.u}$	116 Ω	0.046 $\Omega$	115 Ω	0.022 $\Omega$	114 Ω	0.020 Ω	112 Ω	0.005 Ω	
		$V_{Ref} = -0.1 \text{ p.u}$	183 Ω	0.032 Ω	180 Ω	$0.038~\Omega$	176 Ω	0.043 Ω	170 Ω	0.040 Ω	
		$V_{Ref} = -0.3 \text{ p.u}$	112 Ω	0.023 Ω	107 Ω	0.016 Ω	100 Ω	0.134 Ω	91.8 Ω	0.024 Ω	
	A–B Fault	$V_{Ref} = 0.1 \text{ p.u}$	3.53 Ω	0.012 Ω	4.3 Ω	0.004 $\Omega$	5.46 Ω	0.003 Ω	6.69 Ω	0.004 Ω	
		$V_{Ref} = 0.3 \text{ p.u}$	15.2 Ω	0.026 $\Omega$	17.8 Ω	$0.038~\Omega$	21.2 Ω	0.018 Ω	24.7 Ω	0.039 Ω	
		$V_{Ref} = -0.1 \text{ p.u}$	2.30 Ω	0.018 Ω	2.5 Ω	0.002 Ω	3.67 Ω	0.213 Ω	4.18 Ω	0.114 Ω	
		$V_{Ref} = -0.3 \text{ p.u}$	11.7 Ω	0.018 Ω	13.3 Ω	0.034 $\Omega$	16.1 Ω	0.007 $\Omega$	19.1 Ω	0.101 Ω	
With SCC	A–G Fault	20% Compensation	7.25 <b>Ω</b>	0.004 $\Omega$	7.24 <b>Ω</b>	0.010 Ω	7.28 <b>Ω</b>	0.027 Ω	7.48 <b>Ω</b>	0.040 Ω	
		50% Compensation	18.3 Ω	0.009 Ω	18.3 Ω	0.013 Ω	18.5 Ω	0.026 Ω	18.9 Ω	0.038 Ω	
		70% Compensation	25.5 Ω	0.012 Ω	25.9 Ω	0.020 Ω	26.3 Ω	0.032 Ω	26.8 Ω	0.052 Ω	
	A–B Fault	20% Compensation	11.4 Ω	0.021 Ω	11.5 Ω	0.027 Ω	11.4 Ω	0.048 Ω	11.5 Ω	0.080 Ω	
		50% Compensation	28.7 Ω	0.043 Ω	28.6 Ω	0.017 Ω	28.6 Ω	0.066 Ω	28.7 Ω	0.033 Ω	
		70% Compensation	40.2 Ω	0.005 Ω	40.2 Ω	0.027 Ω	40.1 Ω	0.048 Ω	40.2 Ω	0.091 Ω	

instance, for A–G fault with  $V_{Ref}$  =0.1, the impedance difference decreases from 149 to 140  $\Omega$  when the distance increases from 310 km to 450 km. Finally, the comparison between the conventional relay and the modified relay shows that the modified method decreases the impedance difference to less than 0.2  $\Omega$  for all different types of faults under all operation conditions. For example, the impedance difference with the conventional method is 183  $\Omega$  but is decreased to 0.032  $\Omega$  using the modified method. In other words, the modified method eliminates the SSSC negative effects under any system operation conditions.

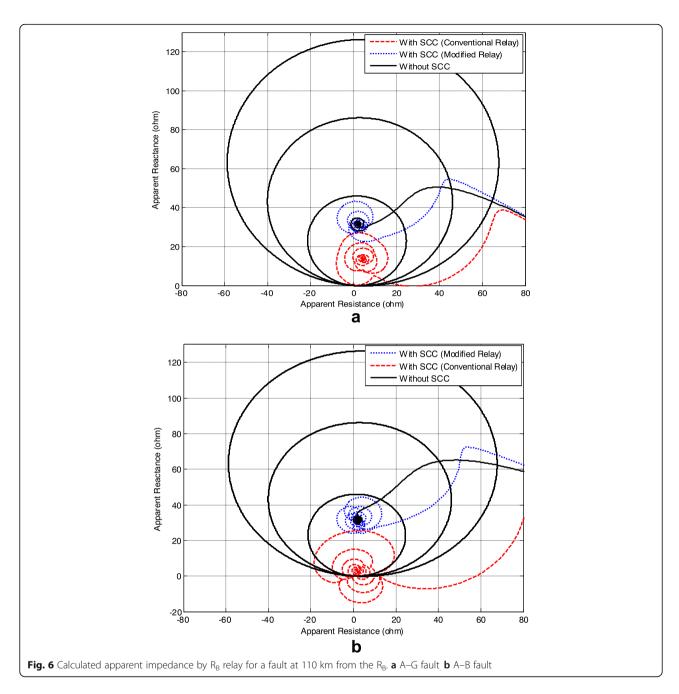
#### With SCC

The simulation results for A-G and A-B faults in the presence of SCC are presented in Fig. 6a and b, respectively. The faults are 110 km away from the relay  $R_B$  and the presented results refer to 50% compensation. As shown, the presence of SCC decreases the relay measured impedance and causes the relay over-reach. Furthermore, using the voltages of both SCC terminals eliminates its effect on the relay measured impedance. Simulation results for different compensations and different fault locations are presented in Table 1. As can be seen, the over-reaching severity increases as the compensation percentage increases. Unlike the SSSC, the SCC effect under phase-to-phase faults is more severe than phase-to-ground faults. The modified method eliminates the SCC negative effects on the relay. For example, the maximum measured impedance difference for 70% compensation during the A-B fault occurring 310 km away from the relay  $R_A$  is 40.2  $\Omega$  and is reduced to  $0.005 \Omega$  with the new modified method.

The results presented above refer to zero fault resistance. Further investigation into the feasibility and ability of the modified method in the presence of the SSSC and SCC under high-resistance faults conditions is carried out in the following sections.

#### High resistance fault

Application of trip boundaries is a trustworthy method for evaluating the effect of fault resistance on the measured impedance by the relay. The fault location and resistance are considered as two important parameters in this method. At the first step, the fault is located at the beginning of the transmission line and the fault resistance  $(R_f)$  is increased from zero to 300  $\Omega$ . This evaluation for the relay  $R_B$  is presented in Fig. 7a, in which the locus follows the path "AB". At the second step, the fault resistance is fixed at 300  $\Omega$  and the location is changed from the beginning to the end of the transmission line. This step makes the path "BC" in Fig. 7a. At the third step, the fault is located at the end of the line and the fault resistance is changed from 300  $\Omega$  to zero which makes the path "CD" in Fig. 7a. At the final step, the fault resistance is fixed to zero and the location is changed from the end to the beginning of the transmission line which makes the path "CA" in Fig. 7a. The increments of  $R_f$  and fault location are set at 30  $\Omega$  and 20 km, respectively. The results presented in Fig. 7 include both capacitive ( $V_{Ref} = 0.3$ ) and inductive ( $V_{Ref}$ = -0.1) operation modes of the SSSC. According to the results presented in Fig. 7, for majority of the capacitive mode the calculated impedance does not fall into the proper protection Zones. The trip boundaries for A-G fault in the presence of 70% SCC are presented in Fig. 7c.



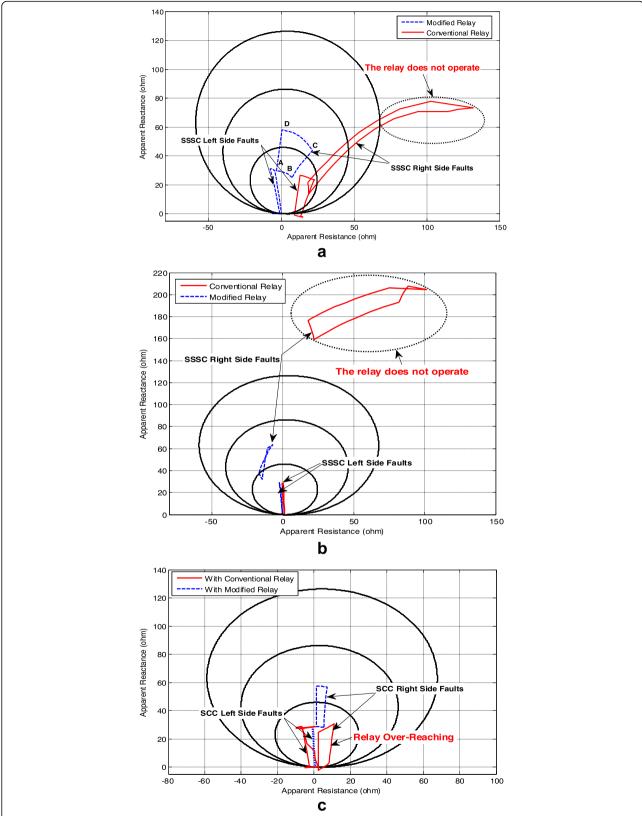
It is worth mentioning that the SSSC changes the trip boundaries more than the SCC does. It is also seen that the presence of series compensator divides the trip boundaries into two parts: part-1 for the faults occurring at the left hand side of the compensator and part-2 for the faults occurring at the right hand side of the compensator. For part-2, the compensator locates in the fault loop. However, when the fault occurs at the left hand side of the compensator (i.e. part-1), the compensator does not locate at the fault loop and only affects the measured impedance if its resistance is not zero.

#### Response time of modified distance relay

The amount of delay can be divided in two parts: the first part is related to PMU calculation (using the mentioned algorithm) and data transmission by fiber optic, and the second part of the delay is related to the protection zones of the distance relay. For the first part delay:

$$t = \Delta t_m + \Delta t_{up} + \Delta t_{syn} + \Delta t_{down} + \Delta t_a \tag{17}$$

where  $\Delta t_m$  is the delay caused by PMU including the time for synchrophasor sampling and calculation.  $\Delta t_{up}$  is



**Fig. 7** Trip boundaries calculated by the A–G element of  $R_B$ . **a** in the presence of SSSC,  $V_{Ref} = 0.3$ . **b** in the presence of SSSC,  $V_{Ref} = -0.1$ . **c** in the presence of SCC (70% compensation)

the delay for transmitting data from PMU to PDC through communication channels.  $\Delta t_{syn}$  is the sum of delay required by PDC to synchronize the PMU data.  $\Delta t_{down}$  is the delay for transmitting the  $\Delta V$  signal from the PDC to the relay through forward channels.  $\Delta t_a$  is the delay required by PDC to synchronize the received data [28]. For the second part of the delay, the distance relay has three operation zones: Zone-1 without delay, Zone-2 has 0.5 s delay and Zone-3 has 1.0 s delay. In other words, if the measured impedance placed in each of the zones, the trip signal will be sent after the related delay. Results are presented for different scenarios in Table 2 where factors such as the fault type, the fault location, the  $V_{Ref}$  of SSSC are considered for modified (MOD) and conventional (CON) methods. As can be seen in the CON method, there are a lot of cases that the relay does not detect fault (shown by "Not Oper"). The numbers shown in Table 2 indicate the amount of delay in the relay operation. These numbers are written in two parts as  $t_1 + t_2$ . The first part  $(t_1)$  is related to the delay of the operation zones of the relay which is adjustable and is used for backup protection and the second part  $(t_2)$  is related to the algorithm delay. The cases that the first part is zero indicate that the relay detects the fault in its first zone (Zone-1) with zero delay. For example, for fault A–B at 295 km of the  $R_B$  relay with  $V_{Ref}$ = 0.3 p.u and using the conventional method, the delay is calculated as  $t_1 + t_2 = 1.0 + 0.114$  s. In other words, the 1.0 s is the delay of Zone-3 and 0.114 s is the delay of the conventional method calculation. As it can be seen that the actual fault is in Zone-2 and is located at 295 km away from  $R_B$  relay but the presence of compensator causes the fault to be falsely detected as in Zone-3 and the amount of  $t_1$  is equal to 1.0 s. Using the modified algorithm, the delay is calculated as  $t_1 + t_2 = 0.5 + 0.422$  s. In other words, the new algorithm avoids the relay maloperation, the fault is properly identified in Zone-2 and the amount of  $t_1$  is equal to 0.5 s. The comparison between the two results shows that  $t_2$  in the conventional method is 0.114 s but in the modified method it is increased to  $0.422~\rm s$  as fiber optic is used. This issue can be compensated by decreasing the protection zones delay which compensates the increased algorithm delay time. As an example, it is possible to decrease the delay of Zone-3 from  $1.0~\rm s$  to  $0.5~\rm s$ .

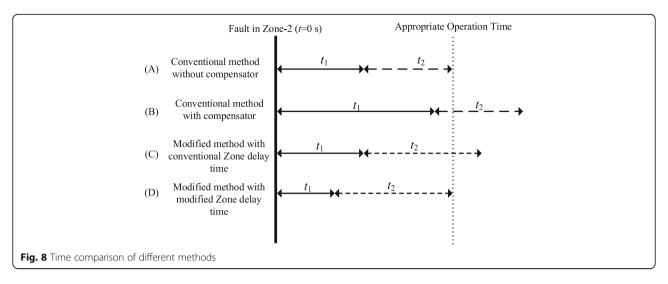
This issue is further explained in Fig. 8. It is assumed that at t = 0 s a fault occurs in Zone-2 and for the conventional method without compensator it is shown by "A". The compensator causes the relay under-reach, the fault is detected as in Zone-3 and causes the delay of zone or  $t_1$  become bigger. This issue is shown by "B" and causes the relay performance time to become longer than the desired value. It has to be considered that the method is similar in "A" and "B" and  $t_2$  becomes equal in these states. However, by applying the modified method (shown by "C"), the relay correctly detects the fault in Zone-2 and  $t_1$  is equal to that in "A". In this method, the calculating time is slightly increased so  $t_2$ become longer and the relay performance time also become slightly longer than the desired value. In "D", the delay time of zone and  $t_2$  are reduced leading to improved relay performance time.

#### Conclusion

The effects of two types of series compensators, SSSC and SCC, on the impedance measured by distance relays have been investigated in this paper. The theoretical and simulation results showed that the presence of these compensators substantially affect the relay operation. The effects of SSSC are more severe than those of SCC, and the SSSC effects under phase-to-ground faults are also more severe than those under phase-to-phase faults. Capacitive and inductive compensations of SSSC make the relay under-reaching and over-reaching, respectively. On the contrary, the SCC always make the relay overreach which is more severe for phase-to-phase faults than phase-to-ground faults. Studies under different SSSC  $V_{Ref}$  conditions and different series compensation percentages showed that the distance relay operation changes due to the presence of SSSC and SCC, which

Table 2 Operation time of CON and MOD relay with the presence of SSSC and considering the set-delay times of zones

	Fault distance from $R_B$	155 km		295 km		400 km			
	Method	CON	MOD	CON	MOD	CON	MOD		
A–G Fault	$V_{Ref} = 0.1 \text{ p.u}$	Not Oper	0 + 0.415	Not Oper	0.5 + 0.433	Not Oper	1.0 + 0.437	Operation time (Sec) of relay	
	$V_{Ref} = 0.3 \text{ p.u}$	Not Oper	0 + 0.414	Not Oper	0.5 + 0.422	Not Oper	1.0 + 0.420		
	$V_{Ref} = -0.1 \text{ p.u}$	Not Oper	0 + 0.415	Not Oper	0.5 + 0.416	Not Oper	1.0 + 0.443		
	$V_{Ref} = -0.3 \text{ p.u}$	1 + 0.128	0 + 0.416	Not Oper	0.5 + 0.416	Not Oper	1.0 + 0.504		
A–B Fault	$V_{Ref} = 0.1 \text{ p.u}$	0.5 + 0.114	0 + 0.412	1.0 + 0.115	0.5 + 0.414	1.0 + 0.112	1.0 + 0.403		
	$V_{Ref} = 0.3 \text{ p.u}$	0.5 + 0.120	0 + 0.426	1.0 + 0.114	0.5 + 0.422	Not Oper	1.0 + 0.418		
	$V_{Ref} = -0.1 \text{ p.u}$	0.5 + 0.116	0+0.418	1.0 + 0.116	0.5 + 0.404	1.0 + 0.112	1.0 + 0.513		
	$V_{Ref} = -0.3 \text{ p.u}$	0.5 + 0.121	0+0.418	1.0 + 0.114	0.5 + 0.406	Not Oper	1.0 + 0.407		



can affect the backup protection of the relay. To eliminate these issues, a PMU-based method was used, in which the voltages of both terminals of the series compensators are measured and transmitted to the relays by communication channels to eliminate the effects of the compensators. Fault resistance using trip boundaries was also studied and it was shown that the series compensators changed trip boundaries and divided them into two parts. Finally, it has shown that by revising the distance relay algorithms the new modified method can easily detect all the faults even for high resistance faults. It has also shown that undesired delay of the new method can be eliminated by changing the set time of the zones of the relay.

#### **Appendix**

The power system parameters are given below:

Voltage Sources:  $V_G$  = 400 kv, 20 deg and 60 Hz,  $Z_{1G}$  = 1.7431 + j19.4150 (ohm),  $Z_{0G}$  = 2.6147 + j4.9009 (ohm),  $V_H$  = 0.95\*400 kv, 0 deg and 60 Hz,  $Z_{1H}$  = 0.8716 + j9.7264 (ohm),  $Z_{0H}$  = 1.3074 + j2.4504 (ohm). Transmission Lines: Line-1 = Line-2 = Line-3 = 200 km and  $Z_{L1}$  =  $Z_{L2}$  =  $Z_{L3}$ ,  $R_1$  = 0.0201 ohm/km,  $R_0$  = 0.1065 ohm/km,  $L_1$  = 7.6076e-4 H/km,  $L_0$  = .0023 H/km.

#### Authors' contributions

New authors have greatly cooperated in preparing the new version of manuscript and answering the reviewer's questions. It wouldn't be fair, if we don't add them to the author list. The article includes three main parts which each one has been accomplished by one of the authors. part1: concept and method, par2: Modeling, part3: Analysis. All authors read and approved the final manuscript.

#### Competing interests

None of the authors of this paper has a financial or personal relationship with other people or organizations that could inappropriately influence or bias the content of the paper.

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Received: 13 July 2016 Accepted: 16 January 2017 Published online: 01 March 2017

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