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A fault segment location method for distribution networks based on spiking neural P systems and Bayesian estimation



Yi Wang¹, Tao Wang^{1,2*} and Liyuan Liu¹

Abstract

With the increasing scale of distribution networks and the mass access of distributed generation, traditional centralized fault location methods can no longer meet the performance requirements of speed and high accuracy. Therefore, this paper proposes a fault segment location method based on spiking neural P systems and Bayesian estimation for distribution networks with distributed generation. First, the distribution network system topology is decoupled into single-branch networks. A spiking neural P system with excitatory and inhibitory synapses is then proposed to model the suspected faulty segment, and its matrix reasoning algorithm is executed to obtain a preliminary set of location results. Finally, the Bayesian estimation and contradiction principle are applied to verify and correct the initial results to obtain the final location results. Simulation results based on the IEEE 33-node system validate the feasibility and effectiveness of the proposed method.

Keywords Distribution network, Fault location, Spiking neural P system, Bayesian estimation, Contradiction principle

1 Introduction

Since they are directly connected with end-users, distribution networks well reflect the needs of customers in terms of security, stability and economy. When a fault occurs in a distribution network, the ability to locate the fault quickly and accurately plays an important role in its safe operation as well as in the quality of electricity supply for users. In recent years, with the access of distributed generation (DG), distribution networks become complex multi-power structures from the original single-power radial type [1, 2]. Consequently, when a distribution network with DG fails, the flow of fault current is no longer in a single direction. Hence, the application of traditional

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At present, many fault location methods have been proposed, such as traveling wave location, impedance and FTU detection-based fault segment location methods. Two traveling wave-based fault location methods are proposed in [6, 7], and while they are fast and accurate, they can be costly and require high sampling rate, leading to complex implementation. In [8, 9], fault location methods based on single and double terminal impedances are proposed. The methods are simple in principle, but they can be influenced by the power supply and line impedance in the system. A high-frequency impedance-based fault location method is proposed in [10], but it can only be applied to neutral-to-ground and phase-to-phase



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faults. In contrast, FTU-based fault location methods are not only simple, but also quick and easy to implement, and thus they are currently very popular. FTU-based fault location methods consist of two types, i.e., matrix algorithm and artificial intelligence. Although the matrix algorithm can improve location speed and accuracy, the accuracy will be affected when the fault information contains distortion. Therefore, artificial intelligence algorithms have gradually attracted more attention.

In recent years, with the rapid development of artificial intelligence, many data-driven and artificial intelligencebased methods have been proposed for fault location, such as expert systems [11, 12], neural networks [13, 14], Bayesian networks [15, 16], cause-effect networks [17, 18], fuzzy set theory [18-20], Petri nets [20, 21], rough set theory [22, 23], and the spiking neural P system (SNPS) [23, 24]. Expert systems are widely used, but their knowledge bases are difficult to design and the reasoning speed is slow. In contrast, neural networks have fast inference speed and good fault tolerance, though they require a large number of training samples, which are hard to obtain. Bayesian networks have clear and intuitive diagnostic models, but it is difficult to obtain prior probabilities with uncertain information. Cause-effect networks have fast inverse reasoning capability, but are susceptible to combinatorial explosion problems and are less fault-tolerant for fault location. Petri networks have the advantage of fast parallel reasoning, but usually suffer from high dimensionality and poor fault tolerance in fault modeling. Although the above methods have their own advantages and application scenarios, the fault segment location for distribution network with DGs still faces many problems, such as large and complex models, high operational dimensionality and poor fault tolerance. Therefore it is necessary to propose new methods to better solve these issues.

SNPS, as a kind of neural-like P system of membrane computing [25], is a bio-inspired artificial intelligence method for fault diagnosis [26, 27]. It has become a hot research topic for fault diagnosis because of its strong distributed parallel computing, image processing and information processing capabilities. It is suitable for fault identification and solving the problem of fault information redundancy in the location process [28]. For example, reference [29] proposes an interval-valued fuzzy SNPS and uses it for transmission grid fault diagnosis. In [30], a new method of transformer fault diagnosis based on learning SNPS with belief AdaBoost is proposed, while [31] proposes a novel fault diagnosis method for smart grids based on memory SNPS considering measurement tampering attacks. These studies show the great potential of SNPSs in solving fault diagnosis problems. Therefore, to improve the fault segment location speed and accuracy of distribution networks with DGs, this paper proposes a fault segment location method for distribution networks based on SNPS and Bayesian estimation. The main contributions of this paper are:

- It proposes a novel fault segment location method for distribution networks, a method which is designed based on SNPS and Bayesian estimation. First, the decoupled single-branch networks are modeled by SNPS with excitatory and inhibitory synapses (SNP-SEIs) and then their matrix reasoning algorithms are employed for segment initial localization. After that, if the initial localization result set is not empty, Bayesian estimation will be used to verify and correct the initial localization result; otherwise, the contradiction principle will be used to identify and correct the distortion information and derive the final location results. Consequently, the method can improve fault location accuracy.
- Since the power directions of distribution networks with DGs are not unique, this paper proposes the use of SNPSEI for the modelling. The inhibitory synapses in the SNPSEI can effectively combine current and voltage fault criteria, so that the model can locate a faulty segment correctly in the case of distorted current and voltage information. Consequently, the SNPSEI-based model can effectively solve location inaccuracy caused by information distortion.

2 Spiking neural P systems with excitatory and inhibitory synapses

2.1 Spiking neural P systems with excitatory and inhibitory synapses

An SNPS with excitatory and inhibitory synapses (SNP-SEI) of degree $m \ge 1$ is a construct of:

$$\mathbf{T} = (O, \sigma_1, \sigma_2, ..., \sigma_m, syn, in, out)$$

where.

- O = {a} is a set of singleton alphabets, and a denotes a spike.
- (2) $\sigma_1, \sigma_2, ..., \sigma_m$ are neurons in the system, and each neuron $\sigma_i (1 \le i \le m)$ is of the form $\sigma_i = (\alpha_i, \kappa_i, R_i)$, where:
- (3) $\alpha_i \in \{-1, 0, 1\}$ is the quantity of electric charges carried by the spike in neuron σ_i
- (4) κ_i ∈ {0, 1} is the firing threshold value of neuron σ_i;
- (5) $R_i = \{r_1, r_2, r_3\}$ is a finite set of rules in neuron σ_i , which are as follows:
- (6) $r_1 : E = (\alpha_i \ge \kappa_i)/a^{\alpha_i} \to a^{\varepsilon}$ is a firing rule, where a^{α_i} denotes the spike that is consumed for executing the firing rule. $\varepsilon \in \{0, 1\}$ denotes the

quantity of electric charges carried by the newly produced spike. It means that if and only if the quantity of the electric charges in neuron σ_i satisfies the firing condition $\alpha_i \geq \kappa_i$, then the firing rule can be executed. After that, the spike a^{α_i} is consumed and a new spike a^{ε} is produced and sent to all the synapses connected to neuron σ_i . It is worth noting that the transmission of quantity of electric charges does not consume time in the system, i.e., the spike immediately reaches the connected synapses.

- (7) $r_2: E = (\alpha_i < \kappa_i)/a^{\alpha_i} \rightarrow \lambda$ is a forgetting rule, where λ is a null character indicating that no new electric charge is generated. If the quantity of electric charge satisfies $\alpha_i < \kappa_i$, then the forgetting rule will be executed and no new electric charge is generated.
- (8) $r_3: t = \{t_1, \overline{t_1}\}$ denotes the set of synapses, where t_1 and $\overline{t_1}$ denote excitatory and inhibitory synapses, respectively. The neurons before and after the excitatory synapses are called pre-excitatory and post-excitatory neurons, respectively. If a pre-excitatory neuron meets its firing condition, then the corresponding excitatory synapses will work. Likewise, the neurons before and after the inhibitory synapses are called pre-inhibitory and post-inhibitory neurons, respectively. If and only if the quantity of electric charges carried by the spike in a pre-inhibitory neuron equals 0, then the corresponding inhibitory synapses will work.
- (9) $syn \subseteq \{1, 2, ..., m\} \times \{1, 2, ..., m\}$ denotes the connection relation between neurons, where $(i, j) \in syn, 1 \le i, j \le m$ with $i \ne j$.
- (10) $in, out \subseteq \{1, 2, ..., m\}$ represent the sets of input and output neurons, respectively.

The SNPSEI contains two kinds of neurons, namely, proposition neurons and rule neurons. The rule neurons include four types, namely, the "*general*", "*and*", "*xnor*" and "*or*" rule neurons. They are shown in Fig. 1, and their definitions and execution rules are described as follows.

(1) Proposition neuron

A proposition neuron σ_i is represented by a symbol *P* and a blue hollow circle. If a proposition neuron is an input proposition neuron, then its initial electric charge comes from the environment; otherwise, the electric charge comes from the result of the logic operation of its presynaptic rule neurons.



(b)

Fig. 1 Graphical representation of neurons. **a** Proposition neuron, **b** general rule neuron, **c** and rule neuron, **d** xnor rule neuron, and **e** or rule neuron

- Rule neurons
- A "*general*" rule neuron is represented by a symbol *R* and a rectangle. The neuron has only one input but multiple outputs. If its firing condition is met, then the firing rule will be executed.
- An "*and*" rule neuron is represented by a symbol *R* and a rectangle. The neuron has at least two inputs but only one output. If its firing condition is met, then the firing rule will be executed and a new quantity of electric charge ε will be generated, where $\varepsilon = \min{\{\alpha_1, \alpha_2, ..., \alpha_k\}}$.
- An "*xnor*" rule neuron is represented by a symbol *R* and a rounded rectangle. The neuron has at least two inputs but only one output. If its firing condition is met, then the firing rule will be executed and a new quantity of electric charges ε will be generated, where $\varepsilon = \alpha_1 \Lambda \alpha_2 \Lambda ... \Lambda \alpha_k$, and $\alpha_1 \Lambda \alpha_2 = (\alpha_1 \cap \alpha_2) \cup (\overline{\alpha_1} \cap \overline{\alpha_2})$.
- An "or" rule neuron is represented by a symbol *R* and a rectangle. The neuron has at least two inputs but only one output. If its firing condition is met, then the firing rule is executed and a new quantity of electric charge ε will be generated, where $\varepsilon = \max{\alpha_1, \alpha_2, ..., \alpha_k}$.

2.2 Matrix reasoning algorithm

To make SNPSEI capable of inferring and processing fault information in a parallel way, a matrix reasoning algorithm is designed, as shown in Algorithm 1.

To improve the readability of Algorithm 1, the vectors and matrices covered are described below.

 $\varepsilon = \min\{\alpha_1, \alpha_2, \dots, \alpha_n\}$

(c)

Algorithm 1 Matrix Reasoning Algorithm for SNPSEI

Input: $\boldsymbol{\alpha}_0$, $\boldsymbol{\delta}_0$, \mathbf{C}_{R1} , \mathbf{C}_{R2} , \mathbf{C}_{R3} , \mathbf{C}_{R4} , \mathbf{C}_P

Step 1: set termination conditions: $\mathbf{0}_1 = \{0,...,0\}_p^T$, $\mathbf{0}_2 = \{0,...,0\}_a^T$;

Step 2: set the number of reasoning steps g = 0;

Step 3: while $(\alpha_{\alpha} \neq \mathbf{0}_{1} \text{ or } \delta_{\alpha} \neq \mathbf{0}_{2})$ **do**;

Step 4: for every input neuron (g=0) or every proposition neuron (g>0) do;

Step 5: if the threshold condition and firing condition $(a^{\alpha_i}, \alpha_i \ge \kappa_i, 1 \le i \le p)$ are satisfied **then**

Step 6: this neuron is fired and δ_{g+1} is calculated according to equation

 $\delta_{g+1} = (\alpha_g \otimes \mathbf{C}_{R1}) + (\alpha_g \oplus \mathbf{C}_{R2}) + (\alpha_g \wedge \mathbf{C}_{R3}) + (\alpha_g \odot \mathbf{C}_{R4})$ Step 7: end if

Step 8: if the proposition neuron has post-synaptic rule neurons **then**

it sends a new electric charge to its post-synaptic neurons;

Step 9: else it will accumulate quantity of electric charges;

Step 10: end if

Step 11: end for

Step 12: for every rule neuron do; Step 13: if the threshold condition and firing condition $(a^{\delta_i}, \delta_i \ge \kappa_i, 1\le i \le q)$ are satisfied then

Step 14: this neuron is fired and α_{g+1} is calculated

according to equation $\boldsymbol{\alpha}_{g+1} = \boldsymbol{\delta}_{g+1} \odot \mathbf{C}_p$; Step 15: end if

Step 16: end for

Step 17: g = g + 1

```
Step 18: end while
```

Output: quantity of electric charges carried by output neurons

- (1) $\boldsymbol{\alpha} = (\alpha_1, ..., \alpha_p)^T$ denotes the quantity of electric charge vector of proposition neurons, where $\alpha_i (i = 1, ..., p) \in \{-1, 0, 1\}.$
- (2) $\boldsymbol{\delta} = (\delta_1, ..., \delta_q)^T$ denotes the quantity of electric charge value vector of rule neurons, where $\delta_j (j = 1, ..., q) \in \{0, 1\}.$
- (3) $\mathbf{C}_{R1} = (\gamma_{ij})_{p \times q}, \mathbf{C}_{R2} = (\gamma_{ij})_{p \times q}, \mathbf{C}_{R3} = (\gamma_{ij})_{p \times q}$ and $\mathbf{C}_{R4} = (\gamma_{ij})_{p \times q}$ denote directed synaptic connection matrices from a proposition neuron to a "general", "and", "xnor" and "or" rule neurons, respectively. If there is such a connection, $\gamma_{ij} = 1$; otherwise $\gamma_{ij} = 0$.
- (4) $\mathbf{C}_P = (\gamma_{ji})_{q \times p}$ denotes the directed synaptic connection matrix from a rule neuron to a proposition

neuron. If there is such a connection, $\gamma_{ij} = 1$; otherwise $\gamma_{ij} = 0$.

Next, the operators are introduced as follows:

(1) Multiplication operator \otimes :

The quantity of electric charges of "*general*" rule neurons is calculated by:

$$\begin{cases} \boldsymbol{\alpha} \otimes \mathbf{C}_{R1} = (\phi_1, ..., \phi_q)^T \\ \phi_j = \gamma_{1j} * \alpha_1 + ... + \gamma_{pj} * \alpha_p \\ j = 1, ..., q \end{cases}$$
(1)

(2) Multiplication operator \oplus :

The quantity of electric charges of "*and*" rule neurons is calculated by:

$$\begin{cases} \boldsymbol{\alpha} \oplus \mathbf{C}_{R2} = (\phi_1, ..., \phi_q)^T \\ \phi_j = \min\{\gamma_{1j} * \alpha_1, ..., \gamma_{pj} * \alpha_p\} \\ j = 1, ..., q \end{cases}$$
(2)

(3) Multiplication operator Λ :

The quantity of electric charges of "*xnor*" rule neurons is calculated by:

$$\begin{cases} \boldsymbol{\alpha} \wedge \mathbf{C}_{R3} = (\phi_1, ..., \phi_q)^T \\ \phi_j = (\gamma_{1j} * \alpha_1 \cap \gamma_{2j} * \alpha_2) \cup (\gamma_{1j} * \overline{\alpha_1} \cap \gamma_{2j} * \overline{\alpha_2}) \\ j = 1, ..., q \end{cases}$$
(3)

(4) Multiplication operator \odot :

The quantity of electric charges of "or" rule neurons is calculated by:

$$\begin{cases} \boldsymbol{\alpha} \odot \mathbf{C}_{R4} = (\phi_1, ..., \phi_q)^T \\ \phi_j = \max\{\gamma_{1j} * \alpha_1, ..., \gamma_{pj} * \alpha_p\} \\ j = 1, ..., q \end{cases}$$
(4)

Likewise, the quantity of electric charges of proposition rule neurons is calculated by:

$$\begin{cases} \boldsymbol{\delta} \odot \mathbf{C}_p = (\phi_1, ..., \phi_p)^T \\ \phi_i = \max\{\gamma_{1i} * \delta_1, ..., \gamma_{qi} * \delta_q\} \\ i = 1, ..., p \end{cases}$$
(5)

3 Fault location method

In this section, a fault location method for distribution networks with DGs based on SNPSEI and Bayesian Estimation is proposed, and its flowchart is shown in Fig. 2 with the steps described as follows.

Step (1): Decouple the complex distribution network structure into several single-branch networks according to the equivalent decoupling algorithm proposed in [32].

Step (2): Obtain the voltage and current values of fault lines. First of all, fault lines are selected according to the zero sequence power direction protection. Specifically, if a circuit breaker is open, then the corresponding branch is faulty and will be modeled by an SNPSEI; otherwise, it is not at fault and the location process ends. Subsequently, the current and voltage values of the obtained faulty lines are received from the supervisory control and data acquisition system.

Step (3): Execute the matrix reasoning algorithms of SNPSEI-based models based on the voltage and current criterion to obtain the initial set O_{L_i} of fault location results, where the collected fault voltage and current direction information is the input of the models.

Step (4): If $O_{L_i} \neq \emptyset$, then the Bayesian estimation is applied via (10) to verify and correct the results in O_{L_i} . After the calculation, the segment with the largest probability value in the results is the faulty one. Then, the results are outputted and the location ends.



Fig. 2 Flow chart of fault location method for distribution networks based on SNPSEI and Bayesian Estimation

Step (5): If $O_{L_i} = \emptyset$, then the contradiction principle is applied to find and correct the nodes with distorted fault information. After that, the faulty segments are determined via the revised information sequence and the results are outputted. Then, the location ends.

3.1 Voltage and current criteria

Currently, the fault current crossing alarm signals obtained by the fault indication equipment are usually used as the fault location criterion of the distribution network. Since most of such devices are installed outdoors, they are easily affected by the environment, which may reduce the accuracy and stability of current information. Also, the collected information may be distorted during the communication process. Thus, only using current leads to reduced accuracy of fault location. Consequently, because of the high acquisition stability and low distortion probability of voltage information, this paper introduces voltage distribution characteristics as the fault criteria. In addition, voltage information is combined with current direction information to form a voltage and current dual-criteria model, to improve the fault location tolerance.

(1) Current criterion

In a single-supply system, when the fault resistance increases, the corresponding fault current decreases. Consequently, the fault currents of downstream nodes of the fault point cannot reach the action thresholds of protection. As a result, the numbers 0 and 1 can be used to represent the statuses of switches. However, with the access of a large number of DGs, fault current flows in distribution networks have changed from unidirectional to bidirectional. Therefore, the direction of short-circuit currents provided by the system power supply is specified as positive. The rectification value of the zero sequence current protection is calculated according to:

$$I_{op}^{1} = K_{rel} \cdot 3I_{0\max} \tag{6}$$

where I_{op}^1 indicates the zero sequence I section protection, K_{rel} is a reliability factor, which generally takes a value from 1.2 to 1.3, and $I_{0 \text{ max}}$ indicates the maximum zero sequence current that flows through the protection in a ground fault. This generally takes a value from 2 to 3 A. The current direction information is represented by the numbers -1, 0 and 1, shown as:

$$\Delta i = \begin{cases} 1 \text{ positive fault current} \\ 0 \text{ no fault current} \\ -1 \text{ negative fault current} \end{cases}$$
(7)

(2) Voltage criterion

When a single-phase-to-ground fault occurs on feeder lines, the voltage distribution regularities of different grounding modes are not the same. For the center point non-effective grounding mode, the voltage of the faulty phase will become zero and the voltage of the non-faulty phase will rise to the line voltage. For the faulty singlebranch network, the voltage difference between two adjacent nodes of the faulty section varies a lot, while the voltage differences between the other nodes are almost constant. Therefore, this paper specifies that the common nodes with large variation are represented by 1, while the others are represented by 0. The mathematical expression is shown as:

$$U_{j} = \begin{cases} 0 \, u_{i-j} \approx u_{j-k} \\ 1 \text{ others} \end{cases}$$
(8)

where *i*, *j* and *k* denote the three adjacent nodes, respectively. u_{i-j} and u_{j-k} denote the voltage differences between two adjacent nodes.

3.2 SNPSEI-based model

The network topology of complex distribution networks is characterized by multiple branches and variable operational modes. To simplify the model, this paper first decouples the distribution network containing DGs into several single-branch networks, and then models each singlebranch network by an SNPSEI. Figure 3 shows a singlebranch network, which can be used to build a common model for fault location based on SNPSEI. For the segment L_n , its SNPSEI-based model is shown in Fig. 4, where proposition neurons are associated with circuit breakers, isolated switches and segments. Rule neurons are represented by rectangular boxes and rounded rectangular boxes, while excitatory synapses are represented by normal arrows and inhibitory synapses are denoted by hollow arrows. It is worth noting that the SNPSEI-based model of a singlebranch network can be obtained by superposing several segment models. The physical meaning of each symbol in the model is shown in Table 1.

3.3 Location result correction based on bayesian estimation

Since most of end instruments and communication devices of distribution networks are outdoors, the collected information may be distorted. To improve the accuracy of





Fig. 4 A universal fault location model of distribution network based on SNPSEI

fault location, this paper employs the Bayesian estimation method to correct initial location results.

First, it is necessary to make the following assumptions [33]:

- (1) All the segments of a feeder have the same failure probability.
- (2) The information of each node has the same distortion probability *p*, and 0 < *p* ≪ 1. From engineering experience and long-term operational data statistics, *p* is generally taken as 0.05 ~ 0.2.
- (3) Each node is configured with independent feeder terminal units, whose operating states do not affect each other.

Then, set initial information sequence as $I = [S_1, S_2, ..., S_n]$. According to the Bayesian conditional probability, the probability $P(L_i|I)$ of a fault occurring in the feeder segment L_i can be obtained as:

$$P(L_i|I) = \frac{P(L_i)P(I|L_i)}{P(I)}$$
(9)

where $P(L_i)$ is the priori probability, and $P(I|L_i)$ represents the probability that the fault information sequence collected by the system at the fault time of segment L_i is I.

Since P(I) is same for the whole feeder, it is known that the posterior probability of failure in each segment

Table 1 Meaning of symbols in the universal mode

| Symbols | Meanings |
|--------------------------------|------------------------------------------------------------------------|
| R _{CB} | Circuit breaker trips |
| S _n | Current direction information collected at switch S _n |
| <i>S</i> _{<i>n</i>+1} | Current direction information collected at switch S_{n+1} |
| Un | Voltage differ- ence change at node <i>n</i> |
| L _n | Segment <i>L_n</i> is faulty |

depends only on the magnitude of $P(I|L_i)$. To indirectly derive the value of $P(L_i|I)$, $P(I|L_i)$ can be calculated by:

$$P(I|L_i) = \prod_{j=1}^{N_{unequality}} (p)_j \prod_{k=1}^{N_{equality}} (1-p)_k$$
(10)

where $N_{unequality}$ and $N_{equality}$ indicate that the numbers of information sequences are unequal and equal to the actual information sequences collected at the fault time of a segment from set $O_{L_i}(O_{L_i} \neq \emptyset)$, respectively.

3.4 Fault information correction based on the contradiction principle

If the circuit breakers operate correctly and the initial set of location results is empty, then there is a missed location. To address this issue, this section employs the contradiction principle to correct the fault information. Then the corrected fault information sequence pairs are tested one by one to determine the faulty segments.

First, it is specified that for any segment, the nodes close to and far from the main power supply are called the parent and child nodes, respectively. Since all the branches in this paper are single-branch networks, the contradiction principle can be summarized as follows [34] (graphically represented in Fig. 5).

(1) If the child node in the information sequence pair is 1, then its parent node cannot be 0 or -1;

(2) If the child node in the information sequence pair is 0, its parent node cannot be -1.

4 Simulation experiments

MATLAB/Simulink 2020a is used here to simulate singlephase-to-ground faults in the neutral point grounding mode through the arc extinguishing coil for a standard 33-node distribution network with DGs. The standard IEEE 33-node distribution network is decoupled into four single-branch networks as shown in Fig. 6, namely, N_1 :[1,2,19,20,21,22], N_2 :[2,3,23,24,25], N_3 :

[3,4,5,6,26,27,28,29,30,31,32,33] and N_4 :[6,7,8,9,10,11, 12,13,14,15,16,17,18].



Fig. 5 Schematic diagram of the contradiction principle



Fig. 6 Decoupling into four single branch network schematic

In the simulations, the power supply output voltage is set as 10 kV and the uniform parameters of each segment line are set as: $R_1 = 0.013 \Omega/\text{km}$, $R_0 = 0.39 \Omega/\text{km}$, $L_1 = 0.933 \text{ mH/km}$, $L_0 = 4.126 \text{ mH/km}$, $C_1 = 12.74 \mu\text{F/km}$ and $C_2 = 7.75 \mu\text{F/km}$. The length of each segment line is 1.5 km, and the DG capacity is 0.5 MW.

4.1 Simulation experiments

In this subsection, simulations of the single fault and double faults are performed for the single-phase-to-ground fault in the neutral point grounding mode through the arc extinguishing coil with and without DG feeder lines. Zero sequence current values and the adjacent node difference are collected for fault resistance values of 10Ω , 100Ω and 500Ω .

(1) Single fault

When a single fault occurs in a single-branch network, the zero sequence current value at each node and the voltage difference at adjacent nodes are shown in Tables 2 and 3, respectively. Table 2 shows that the fault zero sequence current value decreases as the corresponding fault resistance value increases. When the fault occurs in the case of DG access, there is still a current flowing downstream of the fault point. Table 3 shows that the voltage difference of adjacent nodes before and after the faulty node changes greatly, while that of other nodes is almost unchanged. Consequently, the fault current and voltage information can be obtained via (6) to (8), as shown in Table 4.

(2) Double faults

When double faults occur in a single-branch network, the zero sequence current value at each node and the voltage differences at adjacent nodes are shown in Tables 5 and 6, respectively. From Tables 5 and 6, it can be seen that the variation trends of the zero sequence current magnitude and voltage difference for the double faults are similar to the single one. Data decrease significantly in two places because there are two faulty segments. Similarly, the fault current and voltage

34

10.2

91

8.9

0

0

0

22

10.2

91

8.9

0.1

0.1

Table 2 Zero sequence current values of nodes under single fault in $\ensuremath{N_1}$

| Does it | Fault resistance /Ω | Node | Node zero sequence current value/A | | | | | | | |
|----------------|---------------------------|------|------------------------------------|------|------|------|------|------|--|--|
| contain DG? | | 1 | 2 | 19 | 20 | 21 | 22 | 34 | | |
| Yes | 10 | 11.9 | 11.8 | 11.8 | 11.8 | 10.3 | 10.3 | 10.3 | | |
| | 100 | 8.5 | 8.5 | 8.5 | 8.5 | 9.4 | 9.4 | 9.3 | | |
| | 500 | 4.9 | 4.9 | 4.9 | 4.9 | 7.2 | 7.2 | 7.2 | | |
| No | 10 | 12.5 | 12.5 | 12.5 | 12.5 | 0.1 | 0.1 | 0 | | |
| | 100 | 9.1 | 9.1 | 9.0 | 9.0 | 0.1 | 0.1 | 0 | | |
| | 500 | 4.8 | 4.8 | 4.8 | 4.7 | 0.1 | 0 | 0 | | |

Table 5 Zero sequence current values of nodes under double faults in N_1

19

Node zero sequence current value/A

20

6.7

21

6.7

5.3

3.6

6.7

5.0

36 0

Does it

contain

DG?

Yes

No

Fault

/Ω

10

100

500

10

100

500

resistance

1

94

74

62

11.1

9.9

72

2

9.4 9.4 6.7

7.4 7.4 5.3

6.2 6.2 3.6

11.0 11.0

9.8 9.8 5.0

72

| difforanca | values | of | adiacont | nadac | undar |
|------------|--------|----|----------|-------|-------|

72 36

Table 3 Voltage difference values of adjacent nodes under single fault in N_1

| Does it | Fault resistance/Ω | Node voltage difference value/V | | | | | | |
|---------|-----------------------|---------------------------------|-------------------|--------------------|--------------------|--------------------|--------------------|--|
| DG? | | u ₁₋₂ | u ₂₋₁₉ | u ₁₉₋₂₀ | u ₂₀₋₂₁ | u ₂₁₋₂₂ | u ₂₂₋₃₄ | |
| Yes | 10 | 32.4 | 32.4 | 32.3 | 32.4 | 39.2 | 39.2 | |
| | 100 | 18.1 | 18.4 | 18.0 | 18.2 | 23.7 | 23.8 | |
| | 500 | 9.4 | 9.4 | 9.5 | 9.6 | 10.7 | 10.7 | |
| No | 10 | 5.1 | 5.6 | 5.2 | 5.7 | 27.7 | 28.0 | |
| | 100 | 26.1 | 25.2 | 25.4 | 26.2 | 35.6 | 34.9 | |
| | 500 | 19.4 | 19.3 | 19.1 | 19.0 | 20.5 | 20.7 | |

Table 4 Simulation positioning results

| Does it contain DG? | Fault | Fault information | Locate | |
|---------------------------|----------------------|---------------------|---------------------|-----------------|
| | resistance/ Ω | Current information | Voltage information | result |
| Yes | 10 | [1,1,1,1,-1,-1,-1] | [0,0,0,0,1,0,0] | L ₂₁ |
| | 100 | [1,1,1,1,-1,-1,-1] | [0,0,0,0,1,0,0] | |
| | 500 | [0,0,0,0,-1,-1,-1] | [0,0,0,0,1,0,0] | |
| No | 10 | [1,1,1,1,0,0,0] | [0,0,0,0,1,0,0] | |
| | 100 | [1,1,1,1,0,0,0] | [0,0,0,0,1,0,0] | |
| | 500 | [1,1,1,1,0,0,0] | [0,0,0,0,1,0,0] | |

information can be obtained via (6) to (8), as shown in Table 7.

4.2 Comparative experiment and accuracy test

(1) Comparative experiment

Seven cases are considered here and the proposed method is compared with the two in [28] and [35], and the results are shown in Table 8. Case 1 is a single fault with correct fault information, and all the three methods can locate the fault. However, for case 2, that is a

Table 6 Voltage difference values of adjacent nodes under double faults in N_1

| Does it | Fault resistance /Ω | Node voltage difference value/V | | | | | | |
|---------|---------------------------|---------------------------------|-------------------|--------------------|---------------------------|--------------------|--------------------|--|
| DG? | | u ₁₋₂ | u ₂₋₁₉ | u ₁₉₋₂₀ | <i>u</i> ₂₀₋₂₁ | u ₂₁₋₂₂ | u ₂₂₋₃₄ | |
| Yes | 10 | 32.6 | 32.7 | 32.7 | 16.9 | 16.9 | 40.2 | |
| | 100 | 19.0 | 19.1 | 19.0 | 20.7 | 20.7 | 25.6 | |
| | 500 | 17.7 | 17.7 | 17.7 | 18.7 | 18.7 | 17.2 | |
| No | 10 | 24.2 | 24.2 | 24.1 | 12.3 | 12.2 | 5.4 | |
| | 100 | 31.6 | 31.7 | 31.6 | 35.8 | 35.9 | 40.8 | |
| | 500 | 23.5 | 23.6 | 23.4 | 25.6 | 25.7 | 27.8 | |

double fault with correct information, only the proposed method and the one in [28] can locate successfully. Since both cases 3 and 4 have information distortion, the two methods from [28, 35] have different degrees of locating errors while the proposed method is still effective. For case 5, it is a double fault with two node distortion. It can be seen that only the proposed method locates correctly. For cases 6 and 7, the proposed method can consider both the current and voltage information distortions at the same time, while the other two only use the current information distortion. Consequently, only the proposed method can locate the faults accurately. Therefore, it can be concluded from Table 8 that the proposed method can obtain correct location results for all seven cases. Hence it has higher accuracy and effectiveness.

(2) Accuracy test

To further verify the speed and accuracy of the proposed method, the single fault and double faults are generated randomly 100 times. Both correct and distorted fault messages are considered. Then, the localization accuracy and solution time under 100 runs are obtained statistically [36]. The accuracy test results are shown in Table 9. It is seen that the accuracy of the proposed

| Does it | Fault | Fault informatio | Locate | |
|----------------|--------------|------------------------|------------------------|---------------------------------|
| contain DG? | resistance/M | Current information | Voltage information | result |
| Yes | 10 | [1,1,1,0,0,-1,-1] | [0,0,0,1,0,1,0] | L ₂₀ L ₂₂ |
| | 100 | [1,1,1,0,0,-1,-1] | [0,0,0,1,0,1,0] | |
| | 500 | [0,0,0,0,0,-1,-1] | [0,0,0,1,0,1,0] | |
| No | 10 | [1,1,1,0,0,0,0] | [0,0,0,1,0,1,0] | |
| | 100 | [1,1,1,0,0,0,0] | [0,0,0,1,0,1,0] | |
| | 500 | [1,1,1,0,0,0,0] | [0,0,0,1,0,1,0] | |

Table 7 Simulation positioning results

Table 8 Location results of different methods

| Cases | Information evaluation | Fault segments | Ref. [<mark>28</mark>] | Ref. [35] | Proposed method |
|-------|--------------------------------------------------------------------------|---------------------------------|---------------------------------|------------------------------------------------|---------------------------------|
| 1 | Correct | L ₁₂ | L ₁₂ | L ₁₂ | L ₁₂ |
| 2 | Correct | L3L25 | $L_{3}L_{25}$ | L3L4L25 | L3L25 |
| 3 | Miss mes- sage:S ₁ | L ₂ | $L_{1}L_{2}$ | L ₁ | L ₂ |
| 4 | Error mes- sage:S ₂₁ | $L_{19}L_{22}$ | $L_{19}L_{22}$ | $L_{3}L_{19}L_{22}$ | L ₁₉ L ₂₂ |
| 5 | Miss mes- sage: S ₂ Error message:S ₂₃ | L ₂₃ L ₃₆ | L ₂₄ L ₃₆ | L4L24 | L ₂₃ L ₃₆ |
| 6 | Miss mes- sage: S ₁₉ Error mes- sage:U ₂₂ | L ₂₀ | L ₁₉ L ₂₀ | L ₃ L ₂₀ | L ₂₀ |
| 7 | Miss mes- sage: U_{34} Error mes- sage: S_{20} | L ₃₄ | L ₂₀ L ₃₄ | L ₃ L ₂₀ L ₃₄ | L ₃₄ |

 Table 9
 Accuracy simulation results under 100 failures of this method

| Simulation requirements | Single fault accuracy | Single fault average solving time | Double fault accuracy | Double fault average solution time |
|----------------------------|-----------------------------|--------------------------------------------|-----------------------------|---------------------------------------------|
| Simulation results | 100% | 0.01248s | 100% | 0.01404s |

method is 100% with 100 runs for both the single and multiple faults. Therefore, the proposed method is feasible and effective.

4.3 Case studies

This subsection uses cases 6 and 7 as examples to illustrate how the proposed method works.

(1) Case 6: L_{20} is faulty, while the current information of S_{19} is missed and the voltage information of U_{22} has failed to declare. Since the circuit breaker CB_0 of main power and CB_1 of DG have acted, a fault occurs in the feeder segment with the DG, which is modeled based on an SNPSEI, as shown in Fig. 7. Based on the fault information, the initial quantity of electric charges of the input neurons can be obtained as:

$$\boldsymbol{\alpha}_0 = [1, 1, 0, 1, 0, 1, 0, 0, 1, -1, 0, -1, 1, -1, 0, -1, \mathbf{O}_{1 \times 37}]$$

After performing the matrix reasoning algorithm, when g = 0, the results are:

$$\boldsymbol{\delta}_1 = [1, 1, 0, 1, 0, 1, 0, 0, 1, -1, 0, -1, 1, -1, 0, -1, \mathbf{O}_{1 \times 21}]$$

 $\boldsymbol{\alpha}_1 = [\mathbf{O}_{1\times 16}, 1, 1, 0, 1, 0, 1, 0, 0, 1, 0, 0, 0, 1, 0, 0, 0, 0, \mathbf{O}_{1\times 21}]$

When g = 1, the results are:

$$\boldsymbol{\delta}_2 = [\mathbf{O}_{1\times 16}, 0, 0, 0, 0, 0, 0, 1, 0, 0, 0, 1, 0, 0, \mathbf{O}_{1\times 7}]$$

$$\boldsymbol{\alpha}_2 = [\mathbf{O}_{1\times 32}, 0, 0, 0, 0, 0, 0, 1, 0, 0, 0, 1, 0, 0, \mathbf{O}_{1\times 7}]$$

When g = 2, the results are:

$$\boldsymbol{\delta}_3 = [\mathbf{O}_{1\times 30}, 0, 0, 0, 0, 1, 0, 1, 0]$$

$$\alpha_3 = [\mathbf{O}_{1 \times 46}, 0, 0, 0, 1, 0, 1, 0]$$

When g = 3, the results are:

 $\delta_4 = [O_{1 \times 37}]$

Now, the termination condition is satisfied and the reasoning process ends. The sequence of the output neurons is [0, 0, 0, 1, 0, 1, 0], and the initial location results indicate that faults occur in L_{20} and L_{22} . Then, the result set $O_{(L_{20},L_{22})}$ is tested and corrected using Bayesian estimation. The processes are described as follows.

The initial input neuron sequence collected is I = [1, 1, 0, 1, 0, 1, 0, 0, 1, -1, 0, -1, 0, -1, 0, -1].

(a) If L_{20} is faulty, the correct sequence is $I_{L_{20}} = [1, 1, 0, 1, 0, 1, 0, 1, 1, -1, 0, -1, 0, -1, 0, -1]$. According to (10), it obtains:

$$P(I|L_{20}) = \prod_{j=1}^{2} (p)_j \prod_{k=1}^{14} (1-p)_k = p^2 (1-p)^{14}$$

(b) If L_{22} is faulty, the correct sequence is $I_{L_{22}} = [1, 1, 0, 1, 0, 1, 0, 1, 0, 1, 0, 1, 0, 1, 0, 0, 0, 1, -1, 0, -1]$, and it obtains:

$$P(I|L_{22}) = \prod_{j=1}^{4} (p)_j \prod_{k=1}^{12} (1-p)_k = (p)^4 (1-p)^{12}$$



Fig. 7 Fault location model based on cases 6 and 7

(c) If both L_{20} and L_{22} are faulty, the correct sequence is $I_{L_{20},L_{22}} = [1, 1, 0, 1, 0, 1, 0, 1, 1, 0, 0, 0, 1, -1, 0, -1]$. It obtains:

$$P(I|L_{(20,22)}) = \prod_{j=1}^{3} (p)_j \prod_{k=1}^{13} (1-p)_k = (p)^3 (1-p)^{13}$$

Since $0 , it can be found that <math>P(I|L_{20}) > P(I|L_{(20,22)}) > P(I|L_{22})$. Therefore, the fault section is L_{20} .

Case 7: L_{34} is faulty, while the current information of S_{20} is missed and the voltage information of U_{34} has failed to declare. This feeder segment is modeled as shown in Fig. 7. Based on the fault information, the initial quantity of electric charges of input neurons can be obtained as:

$$\boldsymbol{\alpha}_0 = [1, 1, 0, 1, 0, 1, 0, 1, 0, 0, 0, 1, 0, 1, 0, -1, \mathbf{O}_{1 \times 37}]$$

When g = 0, the results are:

$$\boldsymbol{\delta}_1 = [1, 1, 0, 1, 0, 1, 0, 1, 0, 0, 0, 1, 0, 1, 0, -1, \mathbf{O}_{1 \times 21}]$$

 $\boldsymbol{\alpha}_1 = [\mathbf{O}_{1\times 16}, 1, 1, 0, 1, 0, 1, 0, 1, 0, 0, 0, 0, 1, 0, 1, 0, 0, \mathbf{O}_{1\times 21}]$

When g = 1, the results are:

$$\boldsymbol{\delta}_2 = [\mathbf{O}_{1\times 37}]$$

Now, the termination condition is satisfied and the reasoning process ends. The sequence of the output neurons is [0, 0, 0, 0, 0, 0, 0], which indicates that there is no faulty segment. However, the action information of circuit breakers indicates that there is a fault occurrence. So, it is necessary to apply the contradiction principle to detect and correct the distorted nodes and find the faulty segment.

It is known that the collected current information sequence $[S_{CB_0}, S_1, S_2, S_{19}, S_{20}, S_{21}, S_{22}, S_{CB_1}]$ is [1, 1, 1, 1, 0, 1, 1, -1] and [0, 1] is detected as a contradictory information pair. If S_{20} is -1, then [-1, 1] is also a contradictory information pair; if S_{20} is 1, then the contradiction is eliminated. Therefore, the distortion node is S_{20} , and the correct current information sequence is obtained, namely, [1, 1, 1, 1, 1, 1, -1]. Since [1, -1] satisfies the sequence pair when the fault occurs, the faulty segment is thus L_{34} .

5 Conclusions

This paper proposes a fault location method based on SNPSEI and Bayesian estimation for distribution networks considering distortions of fault information. Decoupling of the distribution networks can effectively reduce the modeling dimension of SNPSEI-based models and simplify the computation. In addition, the proposed SNPSEI is used to develop a parallel reasoning algorithm, which can use both the voltage and current information as the criterion, allowing determination of preliminary location results. Bayesian estimation and the information contradiction principle are introduce to verify and correct the location results. Because of the correction functions of the proposed method for both misdiagnosis and missed diagnosis, its fault location accuracy is effectively improved. The comparative simulation results show the effectiveness, feasibility and speed of the proposed method. Since the influence of meteorological factors on distribution networks is increasing, their prediction or location of faults considering such factors will be studied in the future.

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Author contributions

Formal analysis, YW; Investigation, YW; Supervision, TW; Writing—original draft, YW; Writing—review & editing, TW; Data curation, LL. All authors have read and agreed to the published version of the manuscript.

Availability of data and materials

Data and materials are obtained by program software. This program software is Matlab\Simulink.

Declarations

Competing interests

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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