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# Fast valve power loss evaluation method for modular multi-level converter operating at high-frequency

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#### **Abstract**

There is no common accepted way for calculating the valve power loss of modular multilevel converter (MMC). Valve power loss estimation based on analytical calculation is inaccurate to address the switching power loss and valve power loss estimation based on detailed electro-magnetic simulation is of low speed. To solve this problem, a method of valve power loss estimation based on the detailed equivalent simulation model of MMC is proposed. Results of valve power loss analysis of 201-level 500MW MMC operating at 50Hz~1000Hz are presented. It is seen that the valve power loss of a MMC increased by 12, 40 and 93 % under 200Hz, 500Hz and 1000Hz operating frequency. The article concludes that in a device with isolated inner AC system, MMC operating at higher frequency will be more competitive than typical 50Hz/60Hz MMC with moderate increase of operating power loss and significant reduction of the size of the AC components.

**Keywords:** Modular multilevel converter (MMC), Medium-high frequency, Valve power loss, Conduction power loss, Switching power loss

#### Introduction

The Modular Multi-Level Converter (MMC) has been making its way for wide application in both medium and high voltage applications. The MMC is able to synthesize a very high quality sinusoidal waveform by generating a large number of voltage levels [1–3]. Each IGBT in the sub-modules(SM) is normally switched only at a equivalent frequency of 100-150Hz under Nearest Level Modulation (NLM). As a result, the power loss can be reduced to about 1 % per converter station [4].

Currently, there are no commonly accepted standards for determining the power loss for MMC. The IEC has a draft standard (IEC 62751) in preparation [5]. An intuitive way of obtaining the valve power loss valve power losses of a converter station is comparing the measured power at the AC side and the DC side. However, the difference between these two measured values is small, and a good accuracy will be very difficult to reach [5]. Also,

For MMC, some of the necessary input parameters such as valve currents and switching occasions are difficult to be obtained by purely analytical methods [9]. Reference [10] proposed a method based on piece-wise analytical formulas to evaluate the valve power lossvalve power losses, and accurate conduction power loss can be obtained. But the additional switching loss caused by voltage balancing control is estimated to its upper limit, which makes power loss evaluation result of [10] conservative.

To achieve accurate valve power loss evaluation, electrical variables such as the valve currents and the firing

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such method is not able to observe the distribution of value power loss and the factors that impact valve power losses. For most cases, therefore, the losses have to be estimated from component characteristics, using suitable mathematical models of the converters [6]. From traditional PWM modulated two-level and three-level voltage source converter (VSC), the valve power loss can be obtained analytically [7, 8]. valve power lossvalve power losses The analytical method requires variables such as power factor, dc voltage, valve current and switching occasion of the semiconductors.

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pulses of each IGBT are required. Electro-magnetic simulations are therefore recommended to obtain these variables. Reference [11] proposed interpolating analytically integrable equations to calculate voltage and currents during the switching event so as to accurately represent the switching power losses. But such integrable equations are difficult to realize in simulation tools. By analyzing the simulated voltage and current waveforms, power losses are evaluated with different converter operation points in [12], where the detailed electro-magnetic simulation model of MMC was applied.

The method of [12] is accurate, except that it is based on detailed switching model of MMC and therefore the valve power loss estimation requires significant of simulation time. It may take one week to simulate 5-seconds dynamics of a 401-level MMC [13]. Therefore, the valve power loss evaluation based on detailed switching model is time consuming and inconvenient to analyze the impacts affecting valve power loss of MMC, which typically involves repeatedly simulation. To solve such challenges, a valve power loss evaluation method based on fast MMC simulation model is proposed in this paper.

Operating at ac frequency higher than 50/60Hz, MMC can significantly reduce the size of AC transformer, AC inductor and sub-module capacitors. Such high frequency MMC can bring great benefits to front-to-front connected dc/dc converters [14] which consist of two ac/dc converters coupled by an AC transformer or other AC link such as LCL circuit. There are few literatures analyzing the valve power loss of MMC under high frequency operation [15]. Therefore, it would be beneficial to study the relationship between valve power loss and operating frequency.

In this paper, an accurate and fast valve power loss evaluation method is proposed based on detailed equivalent electro-magnetic simulation of MMC.

#### Method

# Average value model for power loss study Basic operating principles of mmc

Figure 1 (a) shows a three-phase MMC consisting of an upper arm and a lower arm for each phase unit, each arm has N half-bridge sub-modules (denoted as  $\rm SM_1\text{-}SM_N$ ) and one inductor L in series. The topology and control method of MMC has been well studied in [16, 17].

To calculate valve power loss accurately, firing pulses for  $T_1$ ,  $T_2$ , arm current and SM capacitor voltage are required from electro-magnetic simulation results. But, as pointed out in [13], detailed switch model of MMC costs excessively long simulation time. Thus, a fast simulation model of MMC is proposed as a substitution to obtain the above variables for valve power loss.

#### Fast simulation model for MMC

Figure 1 (b) shows schematic of the fast MMC simulation model (taking one phase as an example). In this model, each arm is represented by a controlled voltage source with the magnitude of  $U_{\rm arm}$ .

The user defined component 'Calculate SM Voltages' takes arm current  $I_{\rm arm}$  and firing pulse  $S_{C(1:N)}$  of each SM as the inputs to get capacitor voltage for each SM. The 'Modulation and Voltage Balancing Control' module takes reference voltage  $U_{\rm ref}$  arm current  $I_{\rm arm}$  and capacitor voltage  $U_{C(1:N)}$  as inputs to generate the firing pulses of each sub module [18].

The voltage of SM can be expressed as:

$$U_{Ci}(t) = U_{Ci}(t - \Delta t) + \frac{S_{Ci}}{C} \int_{t - \Lambda T}^{t} I_{SMi}(\tau) d\tau$$
 (1)

In eq.(1),  $\Delta t$  is the integration step,  $U_{Ci}(t)$  is the voltage of SM capacitor,  $U_{Ci}(t-\Delta t)$  is the SM voltage in previous integration,  $S_{Ci}$  is a signal function with values of 1 and 0, which represents switch-on state and bypass state of SM,  $I_{SMi}$  is the arm current flowing into SM.

By trapezoid integral:

$$U_{Ci}(t) = U_{Ci}(t - \Delta t) + \frac{\Delta t}{2} \cdot \frac{S_{Ci}}{C} (I_{armA}(t) + I_{armA}(t - \Delta t))$$
 (2)

 $I_{armA}$  is the upper arm current of phase A and it is the same of  $I_{SMi}$ .

The output voltage of number *i*th SM is:

$$U_{SMi} = U_{coni} + S_{Ci} \cdot U_{Ci} \tag{3}$$

 $U_{coni}$  is the on-state voltage of an IGBT or diode, it exhibits when device is in the conducting state. The onstate voltage depends on its current in a non-linear manner and it is usually represented as a piecewise-linear approximation [19] with a threshold voltage  $U_0$  and a slope resistance  $R_{con}$ , as shown by equation (4):

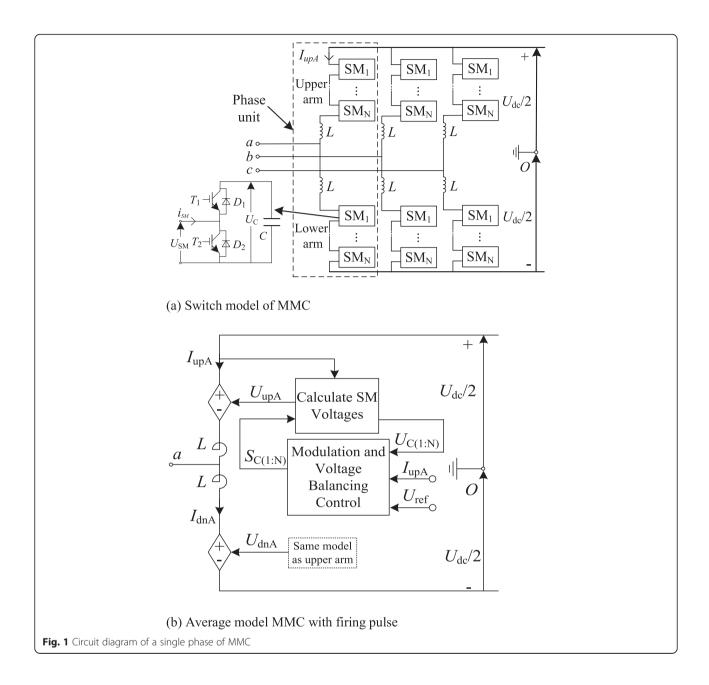
$$U_{coni} = R_{coni} \cdot I_{SMi} + U_{0i} \tag{4}$$

Depending on the directions of arm current and switch on/off state of SM, there are 4 values for  $U_{coni}$ :

$$U_{coni} = \begin{cases} R_{coni\_D1} \cdot I_{SMi} + U_{0i\_D1}, I_{SM} > 0 \text{ and } S_C = 1\\ R_{coni\_T1} \cdot I_{SMi} - U_{0i\_T1}, I_{SM} < 0 \text{ and } S_C = 1\\ R_{coni\_T2} \cdot I_{SMi} + U_{0i\_T2}, I_{SM} > 0 \text{ and } S_C = 0\\ R_{coni\_D2} \cdot I_{SMi} - U_{0i\_D2}, I_{SM} < 0 \text{ and } S_C = 0 \end{cases}$$

$$(5)$$

By superposing voltage of the series SMs in one arm, the voltage output of upper arm of phase A can be expressed as:



$$U_{upA} = \sum_{i=1}^{N} U_{SMi} = \sum_{i=1}^{N} (U_{coni} + S_{Ci} \cdot U_{Ci})$$
 (6)

In.(6), N is the number of SMs in one arm.

Taking this calculated voltage as reference value of the voltage controlled voltage source(VCVS) shown in Fig. 1 (b), a numerical arm of MMC can be established.

This fast simulation model is able to obtain almost identical simulation results as the detailed switching model while simulation speed can be improved by approximately 10,000 times. In addition, it provides the pulse signal of each IGBT, capacitor voltage of

each SM and waveform of arm current for power loss study.

# Valve power loss calculation method based on fast simulation model

# Categories of valve power losses

The power losses are usually estimated from component characteristics, using suitable mathematical models of the converters. The valve power loss can be subdivided into 5 iterms:1) IGBT conduction losses; 2) diode conduction losses; 3) IGBT switching losses; 4) diode recovery losses; 5) IGBT and diode cut-off losses. The formulas for each item of valve power loss has been well

developed in [4–8]. To be concise, they are listed in the Appendix.

#### Junction temperature and parameters adjustment

The valve power loss requires the parameters such as the threshold voltage, conduction resistance and switching energy of the power semiconductors (as presented in (12, 13, 14, 15, 16, 17, 18, 19 and 20) in the Appendix). These parameters are related to the PN junction temperature of power semiconductors. In return, PN junction temperature also depends on the heat generated by loss power. Thus, to accurately estimate valve power loss, power loss and junction temperature of each device need to be calculated iteratively.

Since the junction temperature cannot be measured directly, it will be estimated using a thermal circuit as presented in literatures [12] and [19]. Input for the thermal circuit is the measured temperature of the heat sink while output is the estimated PN junction temperature.

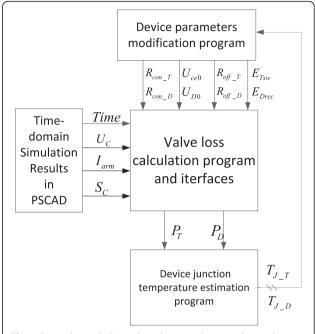
Procedure of iterating the PN junction temperature can be described as follows:

- 1) The heat sink temperature will be measured and an initial value of PN junction temperature is given;
- Based on the present PN junction temperature, parameters such as the threshold voltages, conduction resistance and switching energy will be calculated from the datasheet provided by the manufacture;
- 3) From the obtained parameters of step 2) and the simulated arm currents, sub-module capacitor voltages and the switching state of each sub-module, valve power loss of the power semiconductor will be estimated:
- 4) Based on the estimated valve power loss, the measured heat sink temperature and the thermal circuit of the power semiconductor, a new estimated PN junction temperature is obtained;
- 5) Substitute the obtained new PN junction temperature of step 4) into step 2) and repeat step 2) to 4) until the difference between the previous estimated PN junction temperature and the present estimated PN junction temperature falls within a pre-defined deviation

## Flow chart of valve power loss

A common valve power loss program for MMC is designed in this paper. Flowchart of the program is shown in Fig. 2. It is based on Matlab/GUI with interface to PSCAD/EMTDC.

Firstly, simulation results of fast model in PSCAD are saved as. OUT file. Then the data of capacitor voltages  $(U_c)$ , arm current $(I_{arm})$  and switching states of each submodules $(S_C)$  of the six arms will be read by the 'Valve



**Fig. 2** Power loss calculation based on simulation and considering iunction temperature feedback

power loss calculation program' developed in Matlab. By reading initial parameters of semiconductor devices from 'Device parameters modification program', the power loss and junction temperature of each device can be estimated.

In the next calculation step, the initial parameters of devices are adjusted according to its junction temperature. And then, the power losses are calculated again with the simulation results data of this time step.

Because the switching pattern of each sub-module varies from one fundamental circle to the next, the loss calculation result should be the average value of several fundamental cycles (time duration per circle is 20 ms for 50Hz ac frequency).

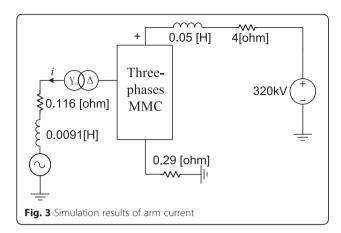
In the simulation results reading program mentioned above, the operating frequency on ac side of MMC can be recognized.

# Parameters design for high frequency MMC

To ensure the valve power loss calculation results under different operation frequency are comparable, the value of SM capacitor and arm inductor should be designed commensurate to the operating frequency.

#### Value of arm inductor against operating frequency

Though arm inductor can be designed to suppress inrush current in transient or suppress circulating current



between the arms, it is usually seen as the connection inductor between ac grid and converter [20].

The apparent capacity (per unit) of connection inductor X can be determined by:

$$S_L(pu) = \frac{I_N^2 X}{V_N I_N} = |\sin \delta_N|, \varphi = 0$$
 (7)

For HVDC application,  $|\delta_N|$  is usually set between 5.7° and 17.5°. Then  $S_{L(pu)}$  should be 0.1 ~ 0.3, thus X can be determined in the range of 0.1 ~ 0.3pu.

Since the per unit value of arm inductor is constant, the actual inductance is inversely proportional to operating frequency. In the following high frequency MMC study, the arm inductance is adjusted according to the operating frequency to make sure that valve power loss calculation results are comparable.

#### Value of SM capacitor against operating frequency

The value of SM capacitor can be determined by the limitation of voltage fluctuation range, illustrated by:

$$C_0 = \frac{\Delta W_{SM}}{2\varepsilon (U_C)^2} \tag{8}$$

Where,  $\varepsilon$  is maximum fluctuation limit percentage of SM voltage and it is usually set as 10 %.  $\Delta W_{SM}$  is the fluctuant energy of the capacitor.

$$\Delta W_{SM}(k) = \frac{2P_S}{kN\omega_0} \left[ 1 - \left( \frac{k\cos\varphi}{2} \right)^2 \right]^{\frac{3}{2}}$$
 (9)

Where,  $P_S$  is the apparent power of MMC and  $\omega_0$  is operating angular frequency. k represents voltage modulation ratio. Substitute eq. (9) into eq.(8), the capacitor of SM can be calculated by:

**Table 1** Converter parameters and operation conditions

Parameters	Value	Comment	
Rated ac voltage	220 kV	Line-to-line	
Equivalent impedance of ac source	0.116Ω + j0.0091H		
Transformer ratio	220/167 kV		
Transformer leakage reactance	0.15 p.u.		
Power rating of MMC	500MVA		
Rated dc voltage	320 kV		
Number of SMs in one arm	200	Redundancy ignored	
Arm inductance	0.6H	0.06H, When 500Hz	
SM capacitance	10000 μF	1000 $\mu\text{F}$ , when 500Hz	
Rated voltage of SM	1.6 kV		
Smoothing reactor	50 mH		
Grounded resistance	0.29Ω		
IGBT module	5SNA 1200E250100	$U_{ce} = 2500V$ $I_c = 1200A$	

$$C_0 = \frac{P_s}{3kN\omega_0\varepsilon(U_C)^2} \left[ 1 - \left(\frac{k\cos\varphi}{2}\right)^2 \right]^{\frac{3}{2}} \tag{10}$$

From eq.(10), it can be concluded that  $C_0$  is inversely proportional to operating frequency under certain operating conditions.

From the information disclosed in [15], we can see that capacitor occupies almost half the volume of a SM. Thus, high frequency MMC can significantly reduce the volume of SMs therefore reduce the volume and weight of a MMC.

#### Results

A three phase Modular Multi-level Converter was modeled in PSCAD/EMTDC. The system layout is shown in Fig. 3 and the parameters under different operating conditions are shown in Table 1.

#### Accuracy verification of the proposed method

To verify the proposed average value model of MMC can be applied for valve power loss, the valve power loss calculation results of fast model, detailed model and analytical method are shown in Table 2. The results are

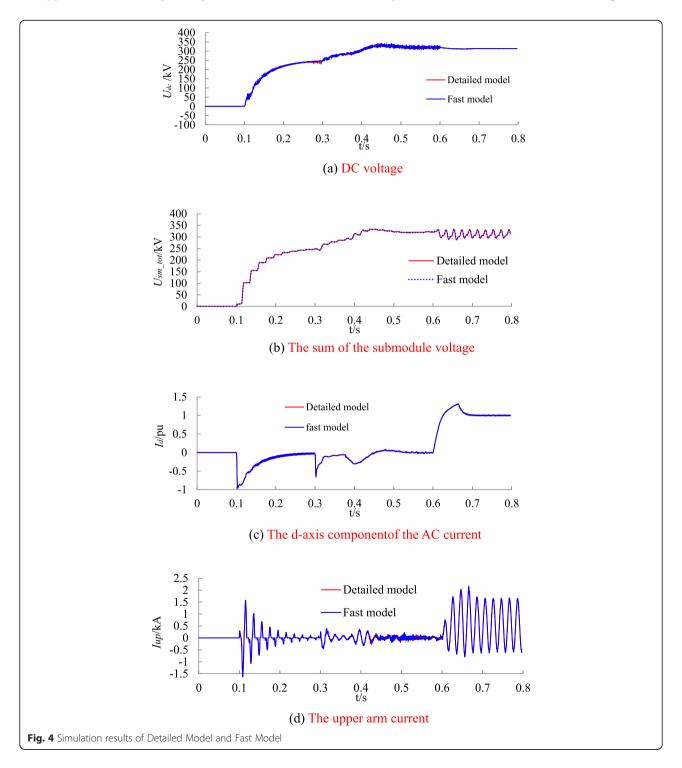
**Table 2** Converter parameters and operation conditions

	Detailed model	Fast model	Analytical method
Switching frequency/Hz	98.5	95.25	95
Switching loss/kW	17.23	17.01	36.24
Conduction loss/kW	444.78	442.20	440.00
Total loss/kW	462.01	459.21	476.24
Simulation time/s	187,045	502.2	<1 s

illustrated as the conduction loss and switching loss of a 50 MW 21-level MMC.

By comparison, the result of detailed model and fast model is shown in Fig. 4.  $U_{dc}$  and  $U_{sm\_tot}$  are the DC voltage and the sum of the Submodule voltage respectively.  $I_d$  and  $I_{up}$  are the d-axis component of the AC current and the upper arm current, respectively.

Apparently, the performance curve of the detailed model and fast model are basically in coincidence. The error of the conduction loss, switching loss and total loss between fast model and detailed model is about 0.58, 1.29 and 0.61 %. Thus, it can be concluded that the fast model of MMC proposed in this paper can provide high accuracy in valve loss calculation. The running time of



the detailed model and fast model is 187,045 s and 502.2 s respectively, which can be concluded that the speed of the calculation process is significantly improved.

On the other hand, the analytical method can also produce an approximate value of conduction loss by applying mean and rms current of each arm. [6] Since switching occasions cannot be resented analytically, the switching loss is estimated by multiplying switching energy and average switching frequency. However, the turn-on and turn-off energy of IGBT and recovery energy of diode should depend on the instantaneous current and dc voltage instead of their mean and rms value. Thus, analytical method presents a large error, about 110 %, on switching loss.

Since the detailed model of MMC takes excessively long simulation time, the further study of this paper is based on the fast model shown in Fig. 1 (b).

#### Verification of MMC models under different frequency

Figure 5 shows the simulation results of the MMC under 50Hz, 200Hz and 500Hz AC frequency while operating at the same power level.

Figure 5 (a) shows the instantaneous voltage of phase A and Fig. 5 (b) shows the active and reactive power output of MMC. It can be seen the voltage magnitudes and power output are exactly the same regardless of the different operating frequencies.

The simulation result of Fig. 5 verifies that the following power loss and comparison of MMC under different operating frequency are carried under same boundary conditions. The only variable is the operating frequency of MMC.

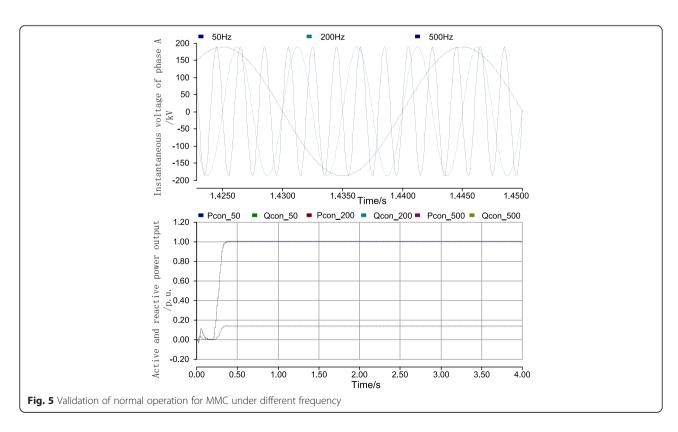
#### Power loss of each device in a SM

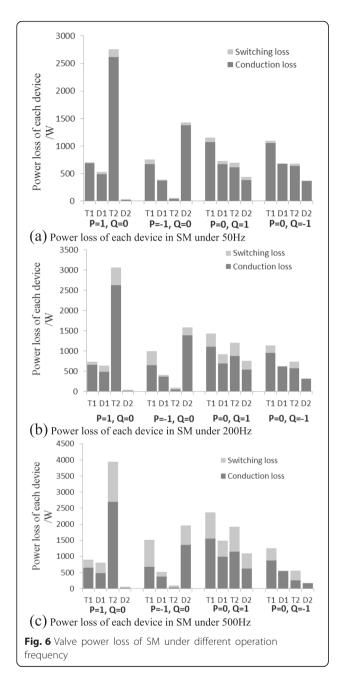
Valve power loss of four typical operation conditions for an MMC, namely, (P, Q) = (1, 0), (-1, 0), (0, 1), (0, -1) are analyzed in this section. Under each condition, the power loss distribution of each IGBT and diode in a SM is different.

Figure 6 shows the calculated power loss of  $SM_1$  on upper arm of phase A under four rated conditions at different AC frequencies. The conduction loss of devices is illustrated in dark grey color and switching loss in light grey.

Figure 6 (a)-(c) show that under the condition of (P, Q) = (1,0), power loss of the lower IGBT $(T_2)$  constitutes the majority of power loss of a SM. Under the condition of (P, Q) = (-1,0), power loss of  $D_2$  is significantly higher than the other three semiconductor switches. Power losses under the conditions of (P, Q) = (0,1) and (P, Q) = (0,-1) are more evenly distributed among the four semiconductor switches.

Figure 6 also shows that increase of the operating frequency only increases the switching power loss while the conduction loss is almost un-affected.

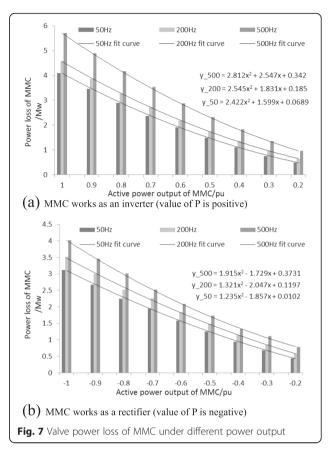




#### Power loss of MMC with different active power levels

This section calculate the power loss under different power output conditions by setting reactive power to be zero and changing the active power from -1.0 p.u. to 1.0 p.u. with step size of 0.1 pu. The power loss results and a second-order polynomial fitting curve between power output and power loss are shown in Fig. 7.

Comparison between Fig. 7 (a) and (b) shows the power loss of a MMC is higher at inverting mode than rectifying mode. Figure 7 also shows the relationship of



the power loss and the active power follows a secondorder polynomial.

Figure 8 shows the percentage of valve power loss power versus the corresponding active power level of the 201-level MMC with power rating of 500 MW.

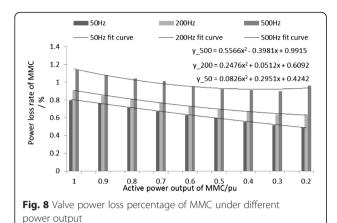
The total power loss at rated power is 4.097 MW at 50Hz AC frequency, which is approximately 0.8 % of rated power. With AC frequency increased up to 200Hz and 500Hz, the power loss ratio increased up to 0.95 and 1.2 % respectively.

#### **Discussion**

To further illustrate the relationship between valve power loss and operating frequency, operating frequency of MMC is varied from 50Hz to 1000Hz at a step of 50Hz. Figure 9 (a) and (b) respectively shows the average equivalent switching frequency and total valve power loss of MMC. The equivalent switching frequency is defined as:

$$f_{sw\_avg} = \frac{1}{T_0} \cdot \frac{1}{6N} \cdot \left[ \sum_{j=1}^{6} \sum_{j,i=1}^{N} (g_{sw\_T1}(i) + g_{sw\_T2}(i)) \right]$$
(11)

In (11),  $g_{sw\_T1}$  and  $g_{sw\_T1}$  is the number of switching instances of IGBT1 and IGBT2 in one sub-module,



during time period  $T_0$ .  $T_0$  is selected to be 1 s to take into effect that switching pattern of a sub-module varies from one fundamental cycle to another fundamental cycle.

It can be seen from Fig. 9 (a) that the equivalent switching frequency increases linearly with operating AC frequency. And switching frequency of a MMC is about 1.8 times of operating AC frequency with the nearest level control modulation and voltage balancing control [21].

Figure 9 (b) shows switching loss also increases linearly with the increasing of operating frequency while

conduction loss remains almost unchanged. Thus, total valve power loss would increase linearly with operating frequency. At the operating frequency of 1 kHz, the switching loss is almost the same as the conduction power loss, making the total power loss of a MMC up to 1.6 %.

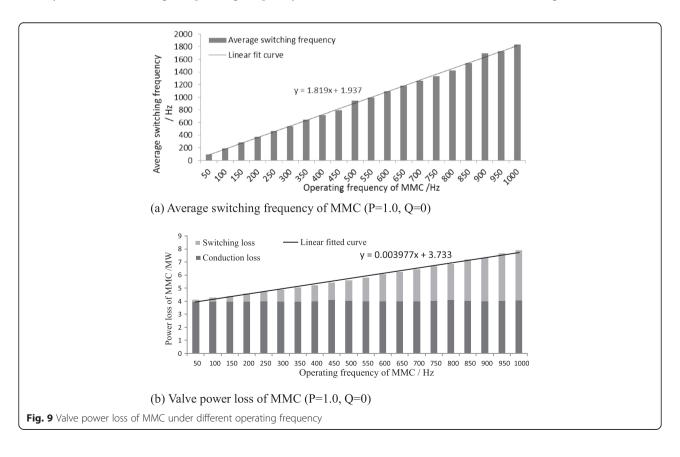
We can empirically conclude from Fig. 9 that a MMC operating at 500Hz AC frequency will increase the total power loss by 40 % while the size of ac inductors and sub-module capacitor can be reduced approximately by 10 times.

#### **Conclusions**

A fast electro-magnetic simulation model of MMC is developed for accurate power loss. It can obtain almost identical simulation results as the detailed switch model while simulation speed can be improved by approximately 10,000 times.

A more accurate program for estimating valve power loss of MMC and junction temperature of devices is proposed, by linking PSCAD/EMTDC and Matlab. The accuracy of power loss calculation is verified by comparison with detailed model of MMC.

Relationships between power losses, the power transfer level and operating frequencies are presented and fitted, with a case of 201-level MMC working at 50Hz  $\sim 1000$ Hz.



Analysis shows that compared with 50Hz operation, the valve power loss of MMC increased by 12, 40 and 93 % under 200Hz, 500Hz and 1000Hz. Since the volume of arm inductor, SM capacitor and transformer of an MMC can be reduced inversely proportional to the operating frequency. Thus, MMC operating at higher frequency will be more competitive than typically 50/60Hz MMC for potential applications in offshore front-to-front connected dc-dc converters.

## **Appendix**

# Conduction losses of IGBT and diode

Denote the on-state voltage as  $U_D$  for diodes and  $U_{ce}$  for IGBTs. The conduction loss can be evaluated mathematically as follows:

$$P_{Tcon} = U_{ce} \cdot I_c = (R_T \cdot I_c + U_{ce0}) \cdot I_c \tag{12}$$

$$P_{Dcon} = U_D \times I_D = (R_D \times I_D + U_{D0}) \times I_D \tag{13}$$

Where  $U_{ce0}$  and  $U_{D0}$  are respectively the threshold voltages for IGBT and diode.  $R_T$  and  $R_D$  are respectively the slope resistance for IGBT and diode.

## Switching losses of IGBT and diode

IGBT switching energies  $E_{\rm on}$  and  $E_{\rm off}$  are normally quoted by the manufacturer as a function of current. Diode switching energies are referred to as recovery energy  $E_{\rm rec}$  and can also be expressed as a function of current.

$$E_{on} = \left(a_1 + b_1 \cdot I_c + c_1 \cdot I_c^2\right) \frac{U_{dc}}{U_{dcN}}$$
 (14)

$$E_{off} = \left(a_2 + b_2 \cdot I_c + c_2 \cdot I_c^2\right) \frac{U_{dc}}{U_{dcN}}$$
 (15)

$$E_{Drec} = (a_3 + b_3 \cdot I_D + c_3 \cdot I_D^2) \frac{U_{dc}}{U_{dcN}}$$
 (16)

The fitting parameters  $a_1$ ,  $b_1$ ,  $c_1$ ,  $a_2$ ,  $b_2$ ,  $c_2$  and  $a_3$ ,  $b_3$ ,  $c_3$  can be found on the datasheet from manufacturer.

The total IGBT and diode switching losses are calculated by summing all the turn-on energies  $E_{\rm on}$  and the turn-off energies  $E_{\rm off}$  or the recovery energies  $E_{\rm rec}$  over an integration time interval  $T_0$ .

$$P_{Tsw} = \frac{1}{T_0} \cdot \sum_{j=1}^{m} \left( E_{Ton}(j) + E_{Toff}(j) \right)$$
 (17)

$$P_{Drec} = \frac{1}{T_0} \cdot \sum_{k=1}^{n} E_{Drec}(k)$$
 (18)

Where, m is the number of switching circles (on and off) experienced by the IGBT during the integration time  $T_0$ , n is the number of recovery occasions experienced by the diode during the integration time  $T_0$ .

#### IGBT and diode cut-off losses

The cut-off power losses are calculated as:

$$P_{Toff} = U_{ce} \cdot I_{c\_leak}(T_j) \tag{19}$$

$$P_{Doff} = U_D \cdot I_{D\_leak}(T_i) \tag{20}$$

The value of leakage current can be found in datasheet from manufacturer and the typical value is 10 mA ( $T_i = 25$  °C) and 60 mA ( $T_i = 125$  °C) for IGBT.

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#### Competing interests

The authors declare that they have no competing interests.

#### Authors' contributions

FT proposed the fast valve power loss estimation method and drafted the manuscript. ZJX implemented the valve power loss estimation algorithm. LZ carried valve power loss estimation of MMC operating under different frequency. CHL designed the parameters of MMC operating under high frequency. JC verified the fast MMC simulation model. JYW designed the case studies. All authors read and approved the final manuscript.

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